

**5V / 3.3V / 12V**  
Page : 35

**1.8V / 0.9V**  
Page : 36

**1.5V / 1.05V / 1.8V**  
Page : 37

**CPU CORE**  
Page : 34

**+1.2V**  
Page : 38

**BATTERY CHARGER**  
Page : 39

**BATTERY SELECT**  
Page : 40

- 5VPCU
- 3V\_ALWAYS
- +12V
- +5V
- 3V\_S5
- 3VSUS
- 5VSUS
- 2.5VSUS
- +2.5V
- +1.8V
- MVREF\_DM
- SMDDR\_VTERM
- 1.5V\_S5
- +1.5V
- AGP\_VCC (+1.5V)
- 1.2VCCT
- VTT
- VCC\_CORE
- VGA\_CORE
- 2.5V\_VGA

**CLOCK GEN ICS**  
**ICS954217**  
Page : 2

**Centrino**  
**DOTHAN**  
**CELEROM-M**  
INTEL Mobile 479 CPU Page : 3 , 4

# CRANE3 ( ZL7 )

ED@ INT. VGA WITH DOCK  
ID@ INT. VGA WITH DOCK  
ND@ W/O DOCKING要打

BOM MARK  
E@ EXT VGA 要打  
I@ INTVGA 要打  
SA@ SATA 要打  
F@ FIXED ODD要打  
SW@ SWAPPABLE ODD 要打  
3@ 3in1 要打  
N@ NEW CARD 要打  
4@ 4401 要打  
5@ 5788M 要打  
D@ DOCKING 要打

**DDR2-SODIMM1**  
Page:9~10

**DDR2-SODIMM2**  
Page:9~10

**ALVISO**  
1257 BGA  
Page : 5 ~ 8

**ATI**  
**M26P/M24P**  
64M / 128M  
Page : 11 ~ 14

**SWITCH CIRCUIT**

**CRT**  
Page:17

**LVDS**  
Page:16

**TV-OUT**  
Page:16

**DVI**  
**CH7307**  
Page:15

**DOCKING/DVI**  
Page: 33

**SATA - HDD**  
Page:21

**IDE - HDD**  
Page:21

**IDE-ODD**  
Page:21

**MEDIA BAY**  
Page:21

**ICH6-M**  
609 BGA  
Page : 18 ~ 20

**NEW CARD**  
Page : 32

**TI**  
**PCMCIA+1394**  
**+3 IN 1**  
**PCI7411**  
Page : 23

**3 IN 1**  
Page: 24

**PCMCIA**  
Page: 24

**1394**  
Page: 23

**AUDIO CODEC**  
**CONEXANT**  
**20468-31**  
Page:27

**AMP**  
**MAX9755**  
Page:28

**MODEM**  
**CONEXANT**  
**20493-21**  
Page:27

**NS**  
**KBC(97551)**  
Page : 29

**NS**  
**SIO (87383)**  
Page : 31

**MINI-PCI**  
Wireless LAN  
Modem/LAN  
Page : 22

**TV-TUNER**  
Page : 22

**BROADCOM**  
**10/100/1G LAN**  
**4401 / 5705M**  
Page:25

**BOTHHAND**  
**TRANSFORMER**  
Page:26

**RJ45**  
Page:26

**MIC IN**  
Page:27

**LINE IN**  
Page:27

**SPEKER**  
Page:28

**LINE OUT**  
Page:28

**RJ11**  
Page:27

**DOCKING**  
**PS2**  
Page:33

**Touchpad**  
Page:30

**Keyboard**  
Page:30

**IrDA**  
Page:31

**DOCKING**  
**Print Port**  
Page:33

**DOCKING**  
**COM Port**  
Page:33

**CIR**  
Page:33

**SYSTEM 3**  
**USB PORT**  
Page : 22


**DOCKING 2**  
**USB PORT**  
Page : 22

**MINI-USB**  
Page: 22

PCI ROUTING TABLE IDSEL INTERRUPT DEVICE

REQ0# / GNT0#	AD24	INTA#	BROADCOM LAN
REQ2# / GNT2#	AD19	INTB# , INTD#	MINI-PCI
REQ1# / GNT1#	AD17	INTC# , INTD# , INTA#	TI 7411
REQ3# / GNT3#	AD18	INTB# , INTD#	MINI-PCI (TV Tuner)

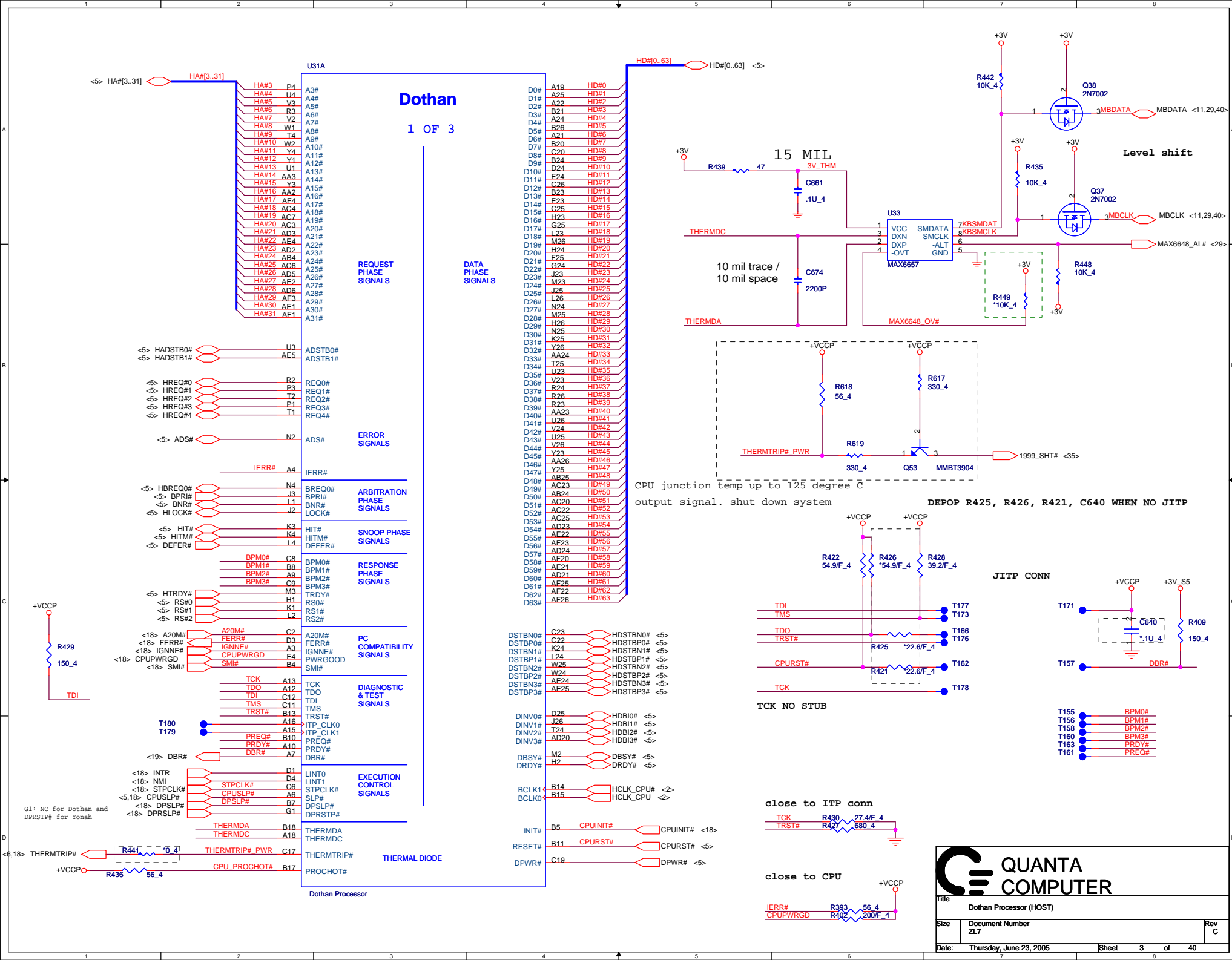
## REV.C



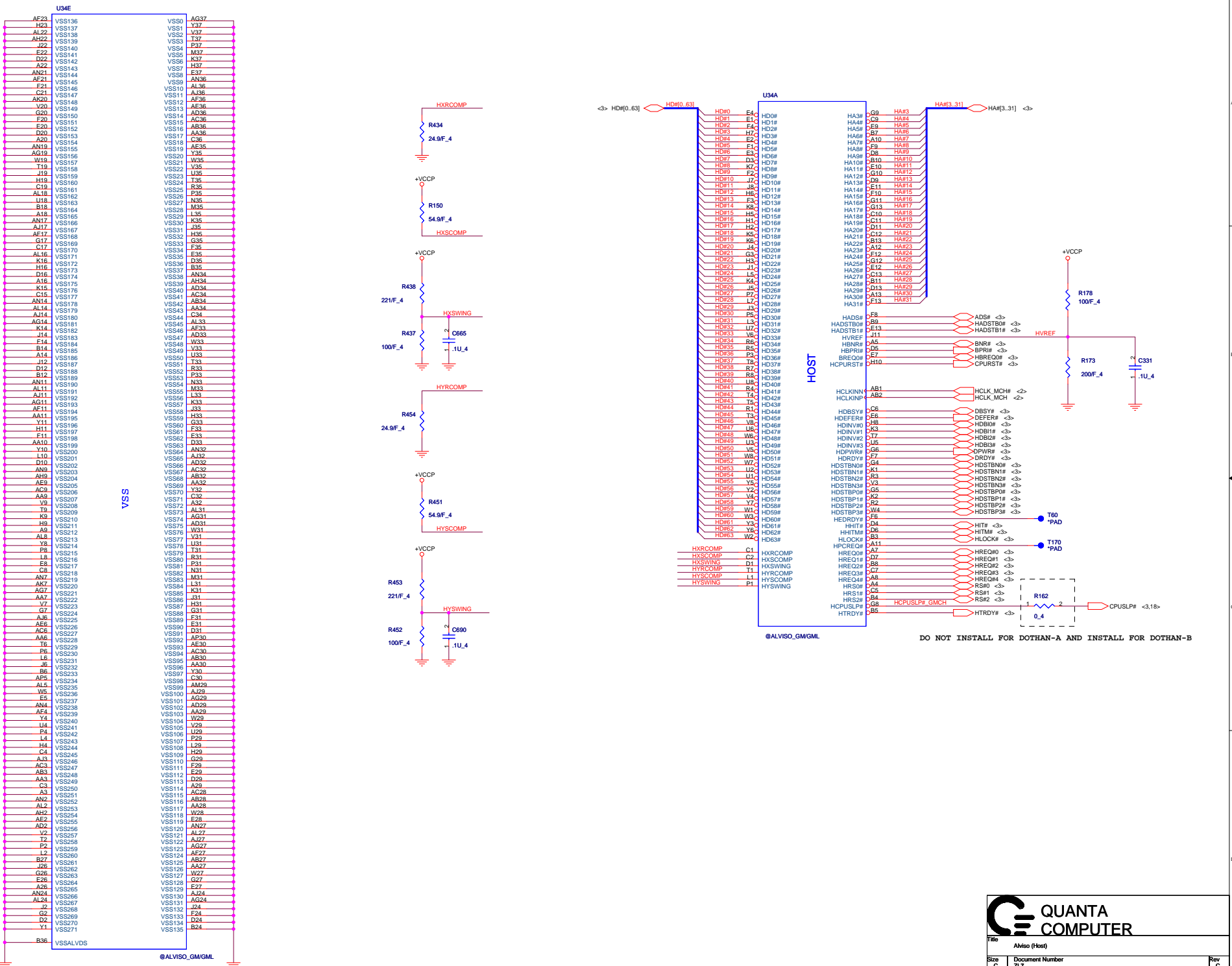
**PROJECT : ZL7**  
**Quanta Computer Inc.**

Size	Document Number	Rev
	<b>BLOCK DIAGRAM</b>	C
Date:	Thursday, June 23, 2005	Sheet 1 of 40



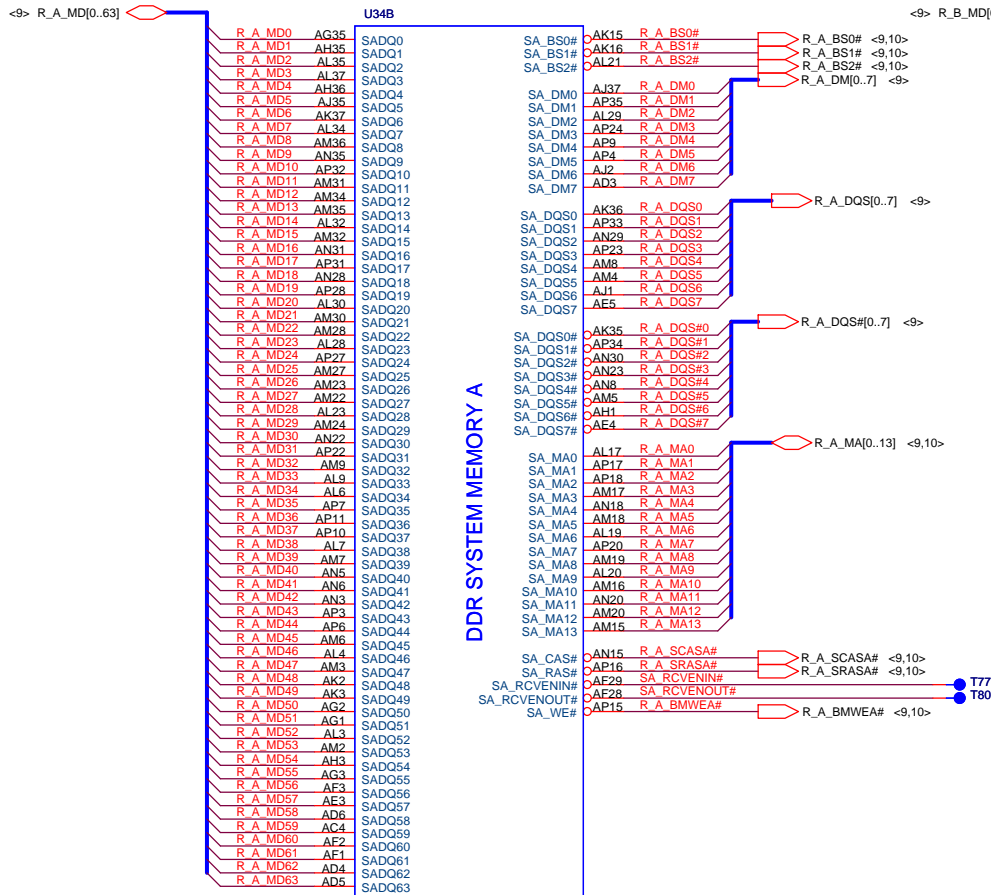




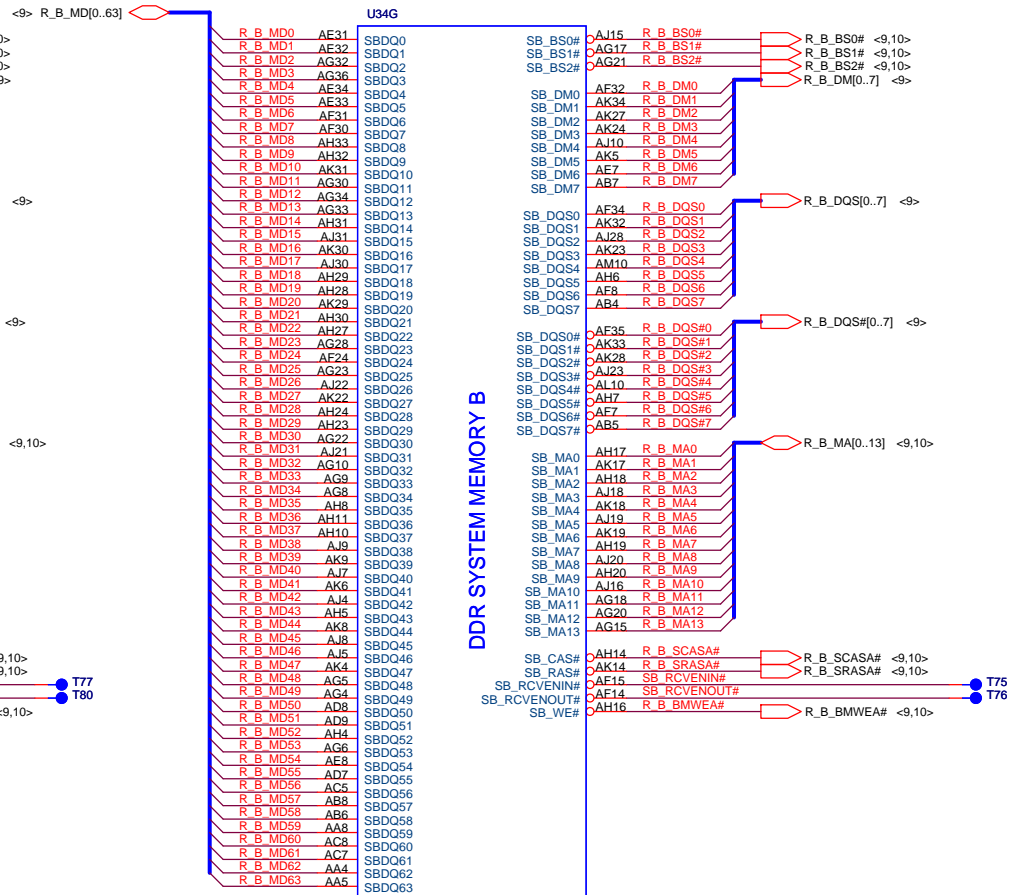




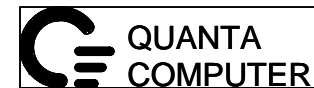




@ALVISO\_GM/GML

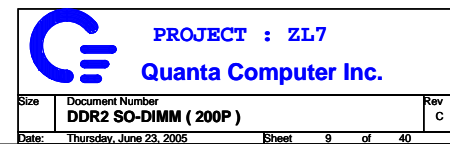
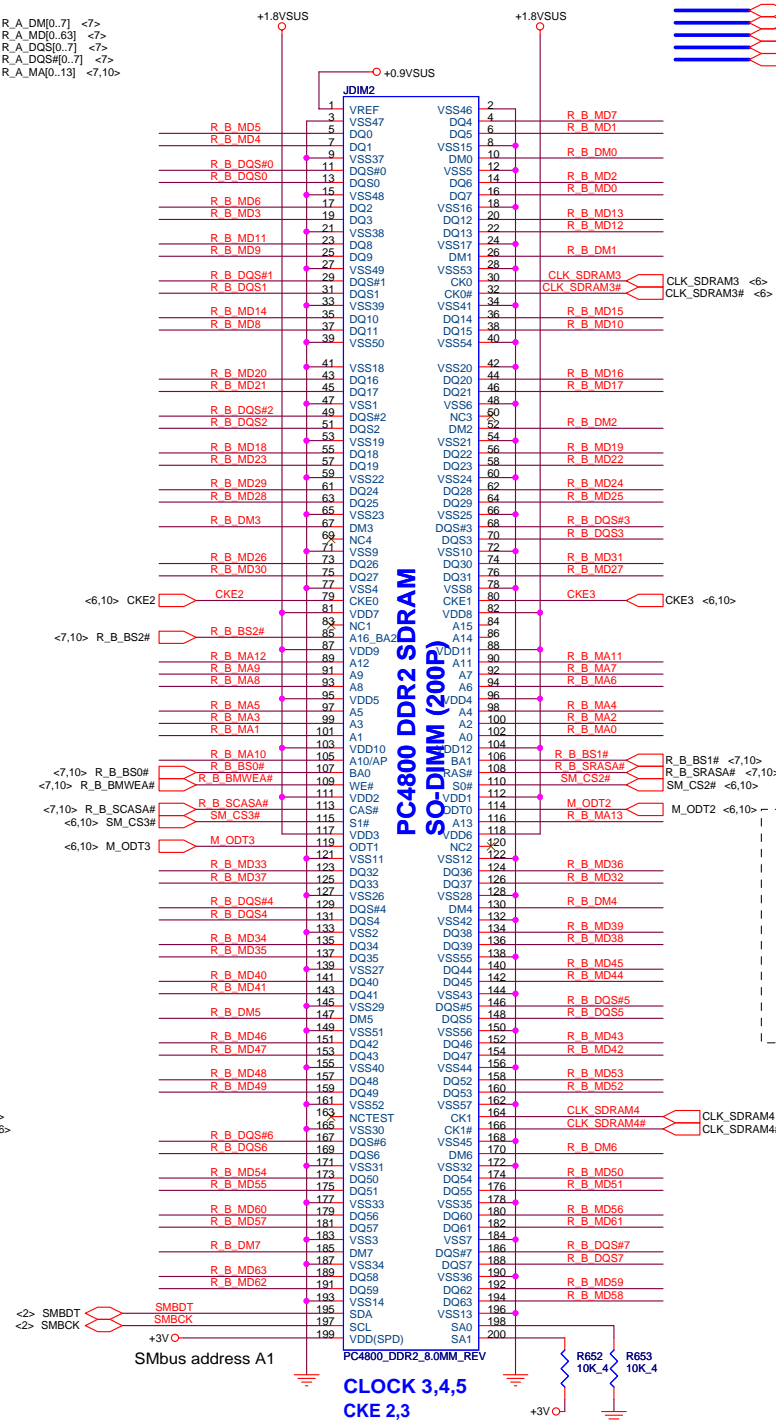


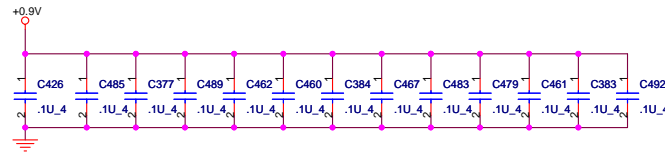
@ALVISO\_GM/GML



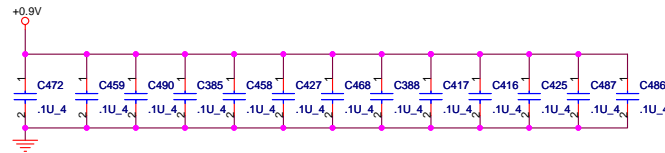




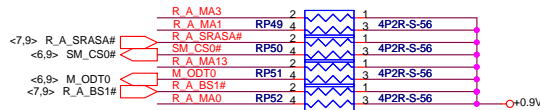
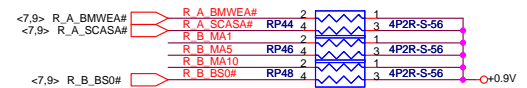
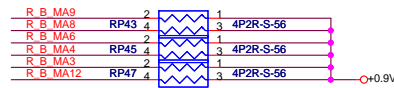
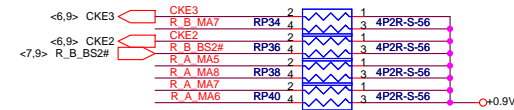
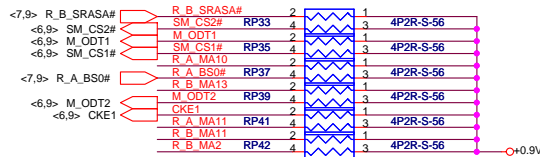
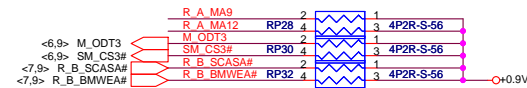
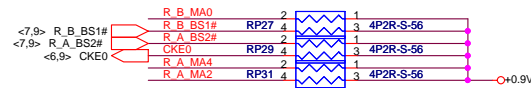


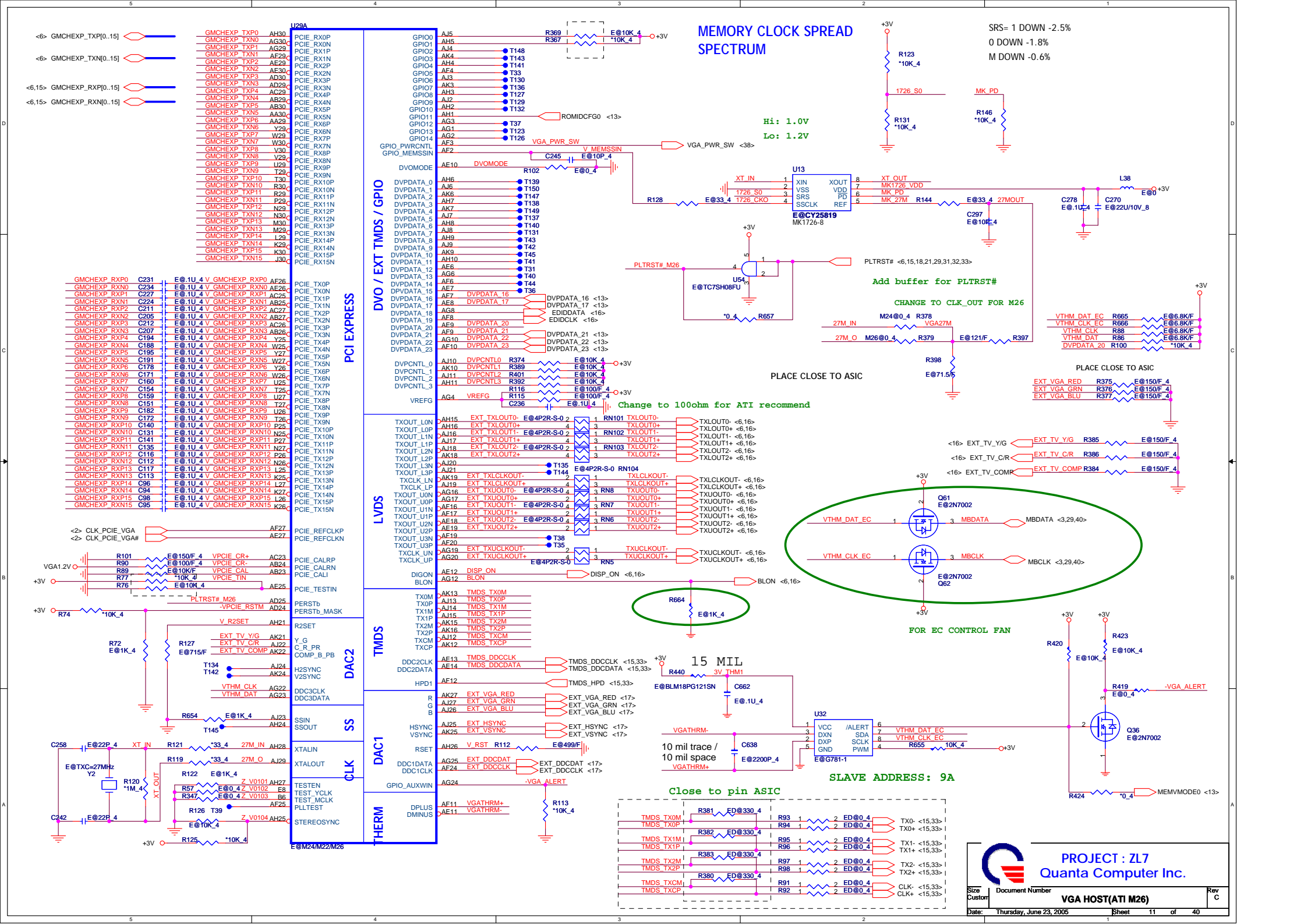


Layout note: Place one cap close to every 2 pullup resistors terminated to +0.9V



Layout note: Place one cap close to every 2 pullup resistors terminated to +0.9V



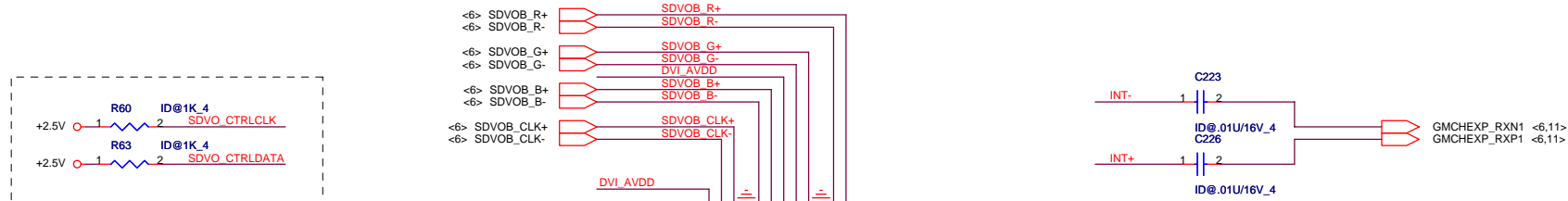






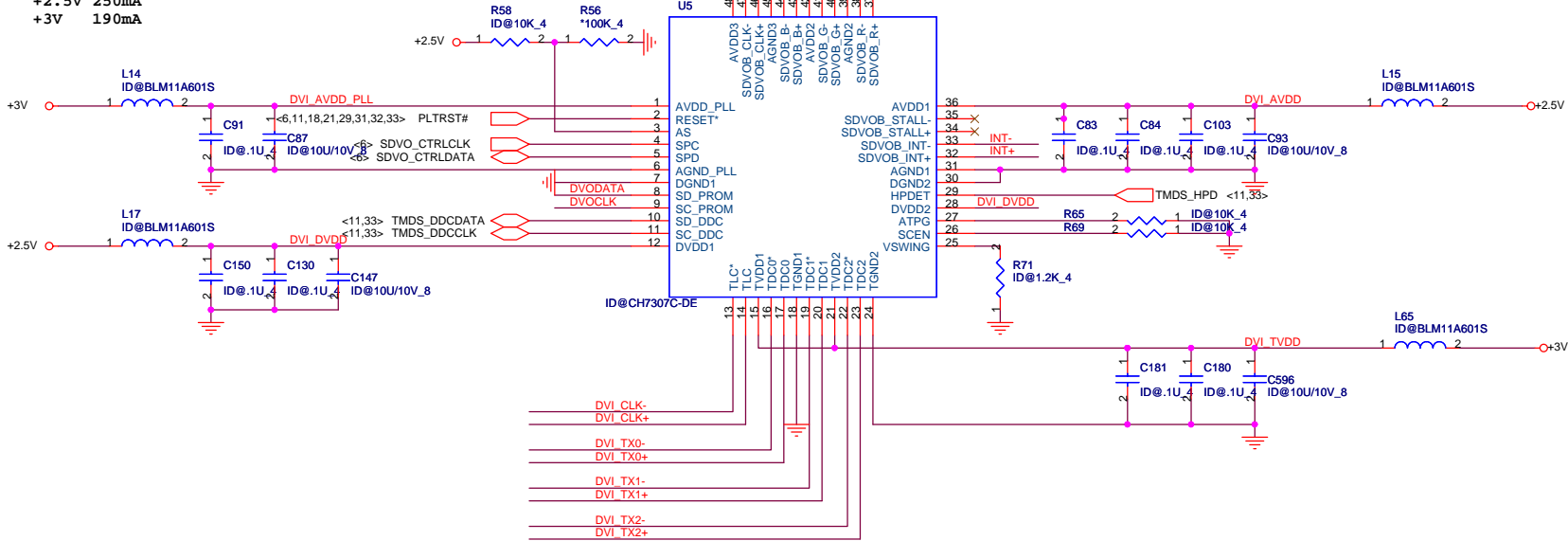




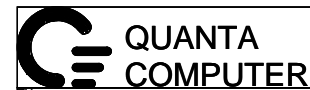
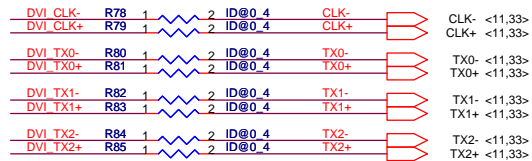
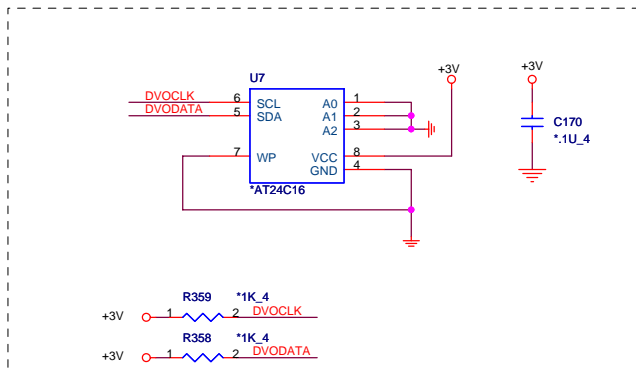


PULL LOW FOR DVO NOT PRESENT (INTERNAL PULLLOW IN 915GM)

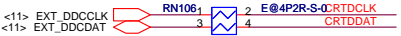
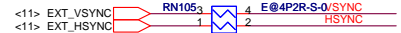
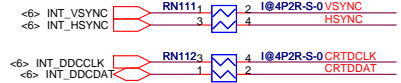
+2.5V 250mA  
+3V 190mA



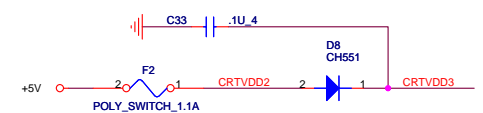
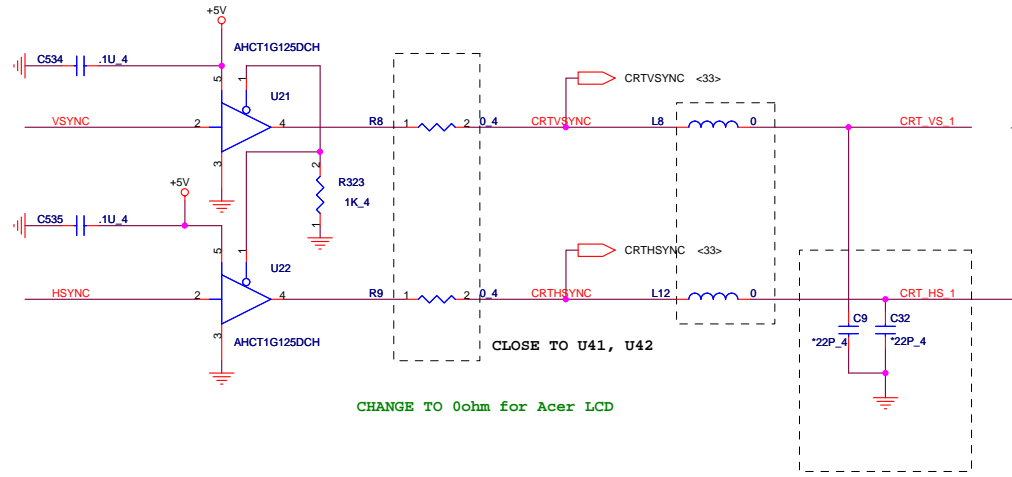
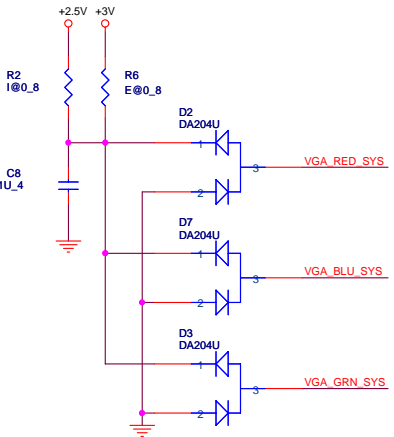
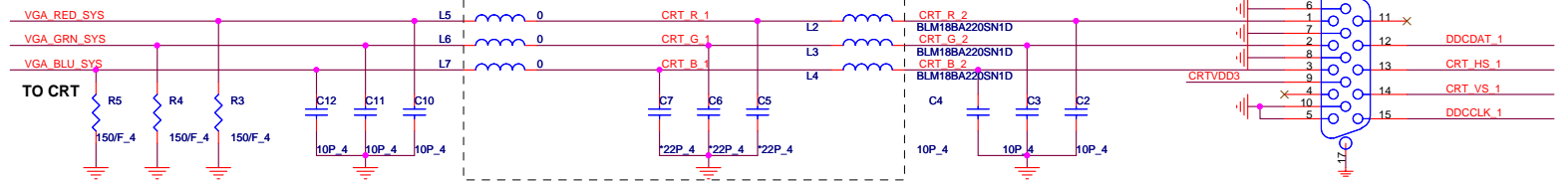
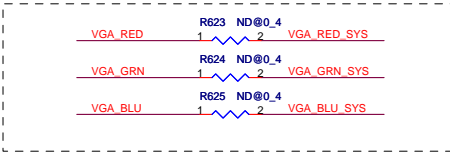
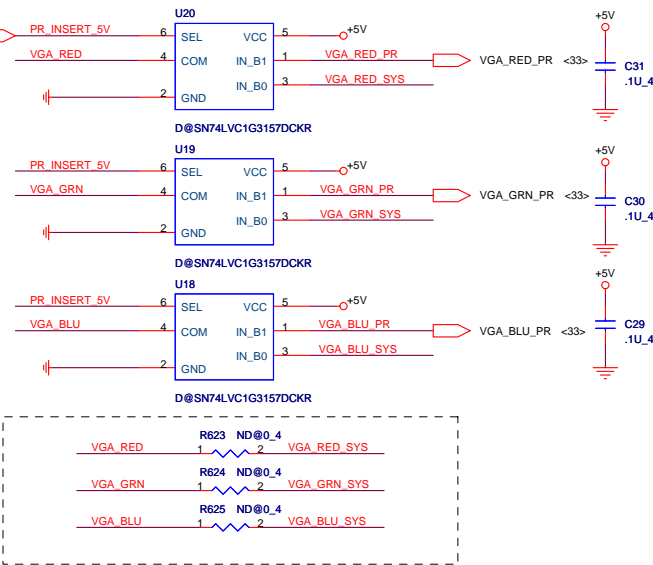
ALWAYS NOT ON, TEST ONLY





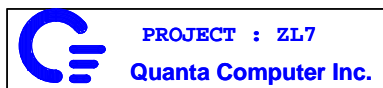


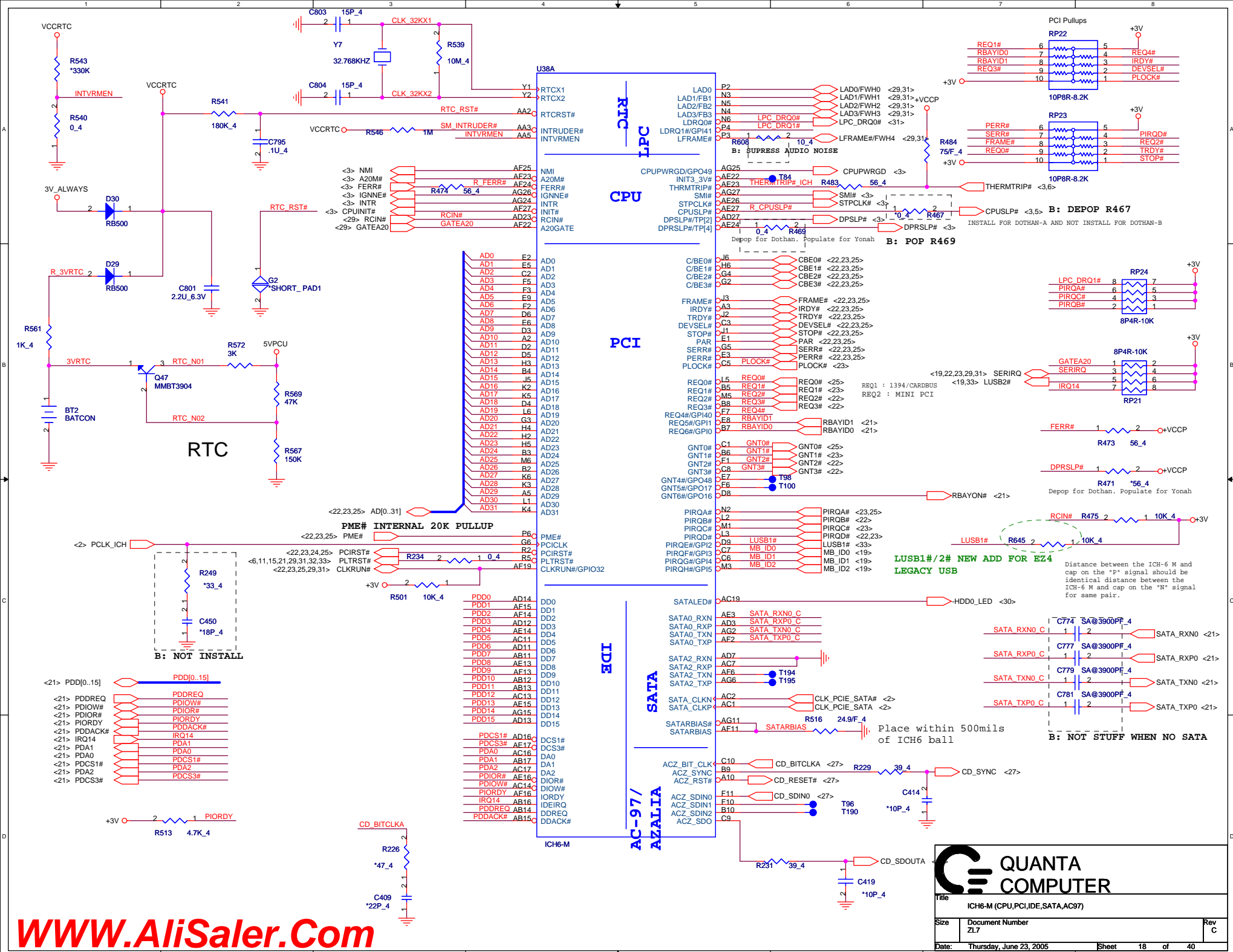
SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1



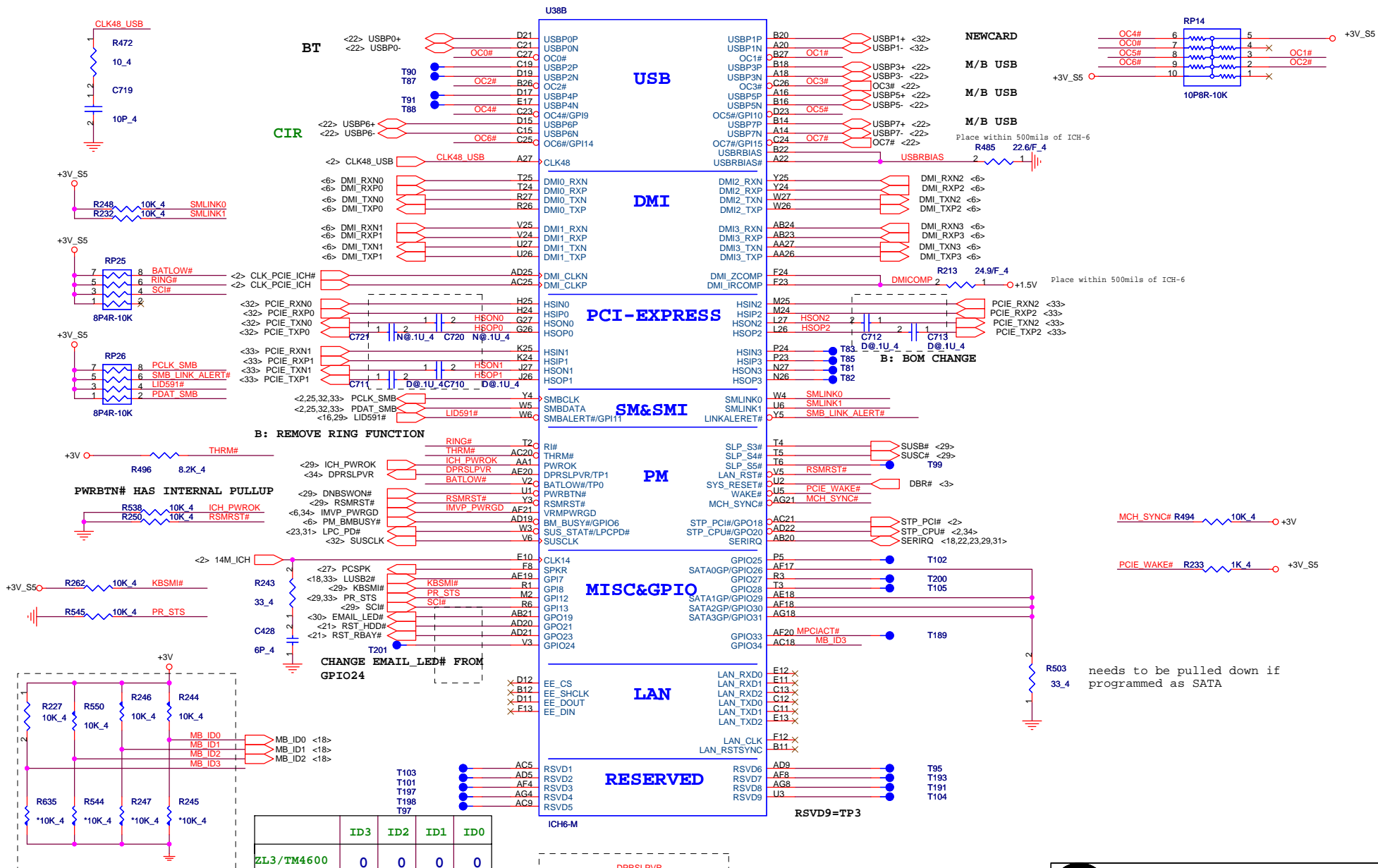
CHANGE TO 0ohm for Acer LCD

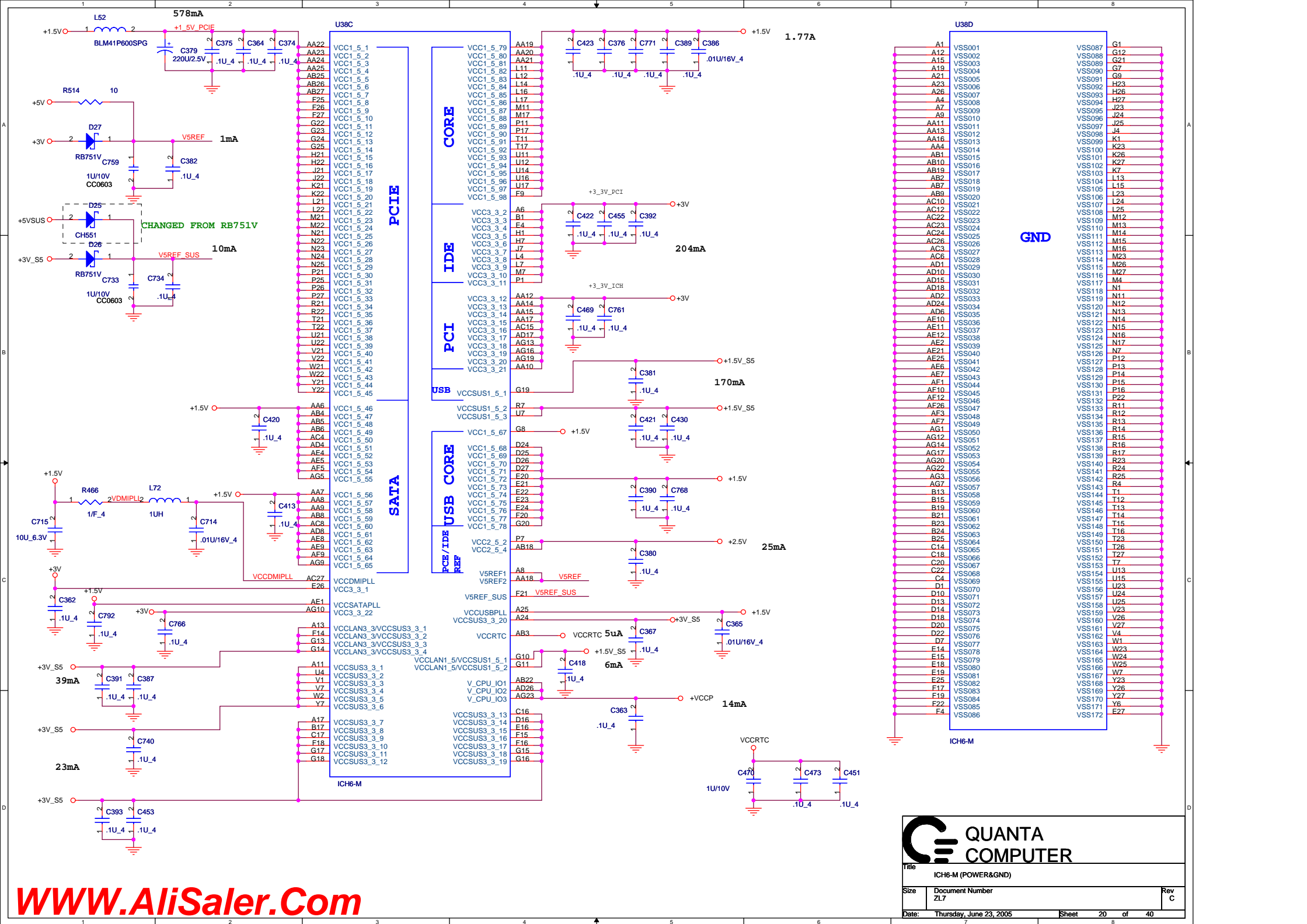
Change to FDV301N for Vgs issue.

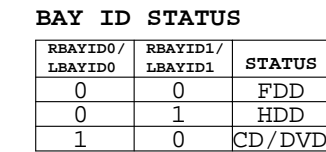
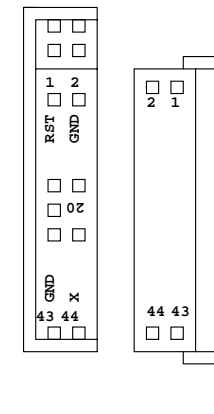







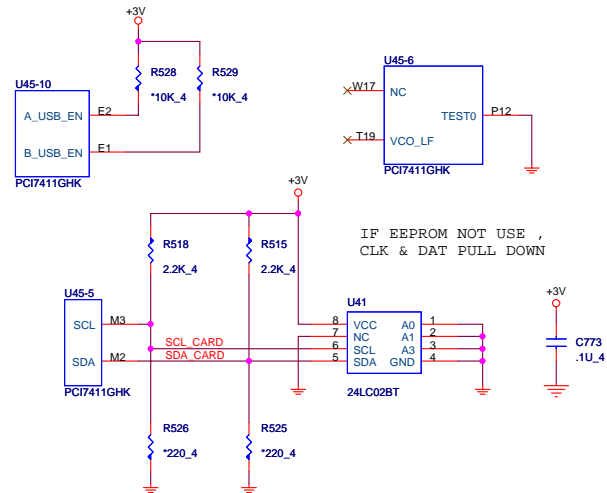
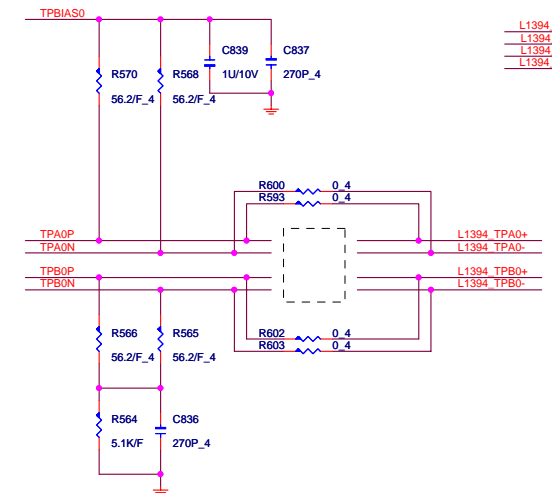
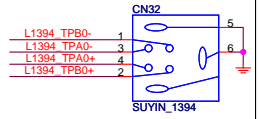
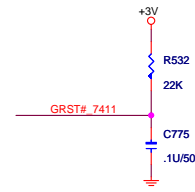
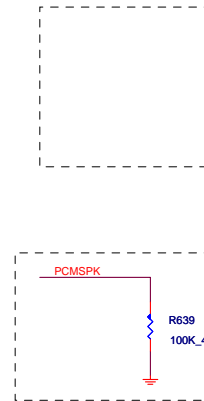




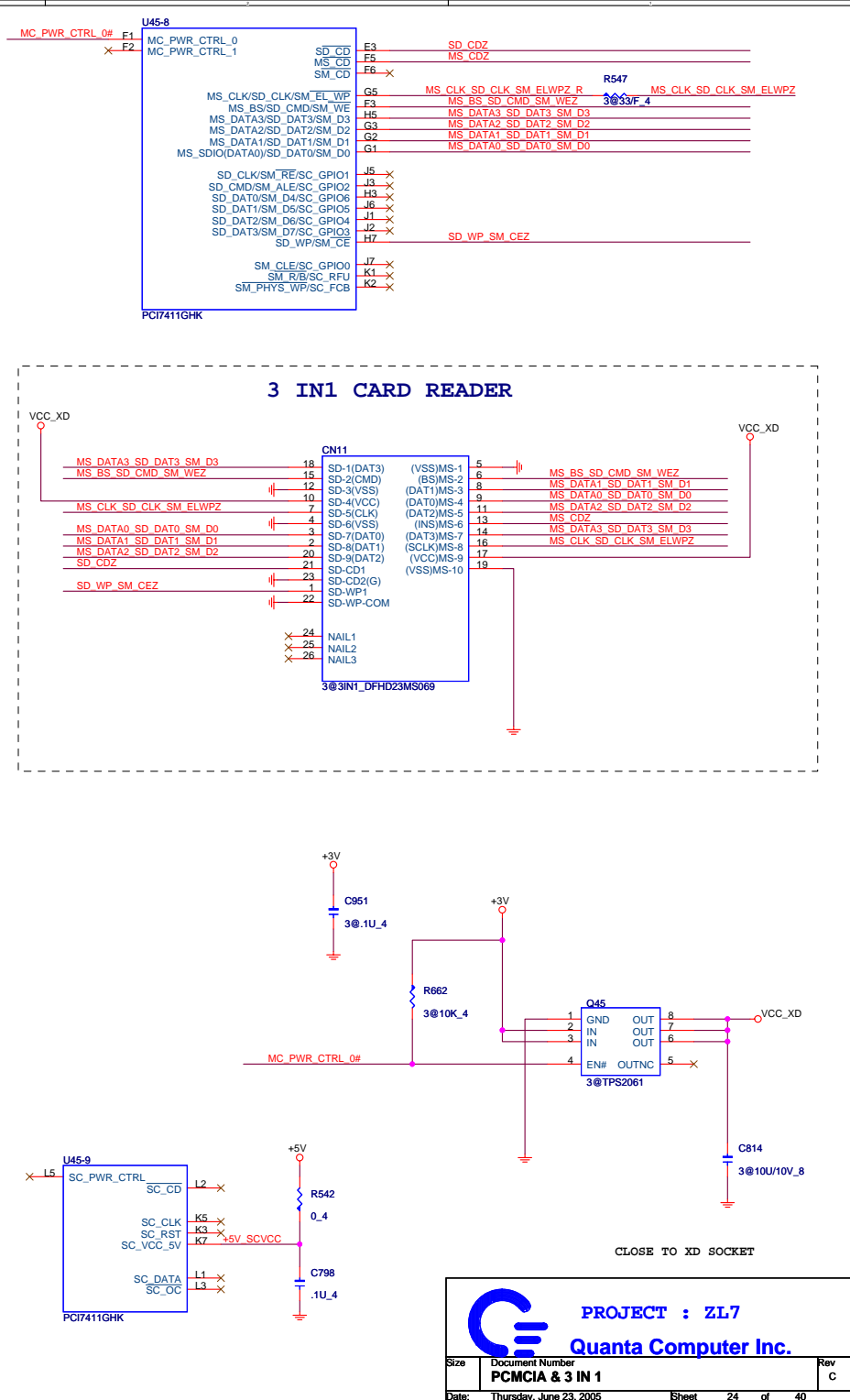
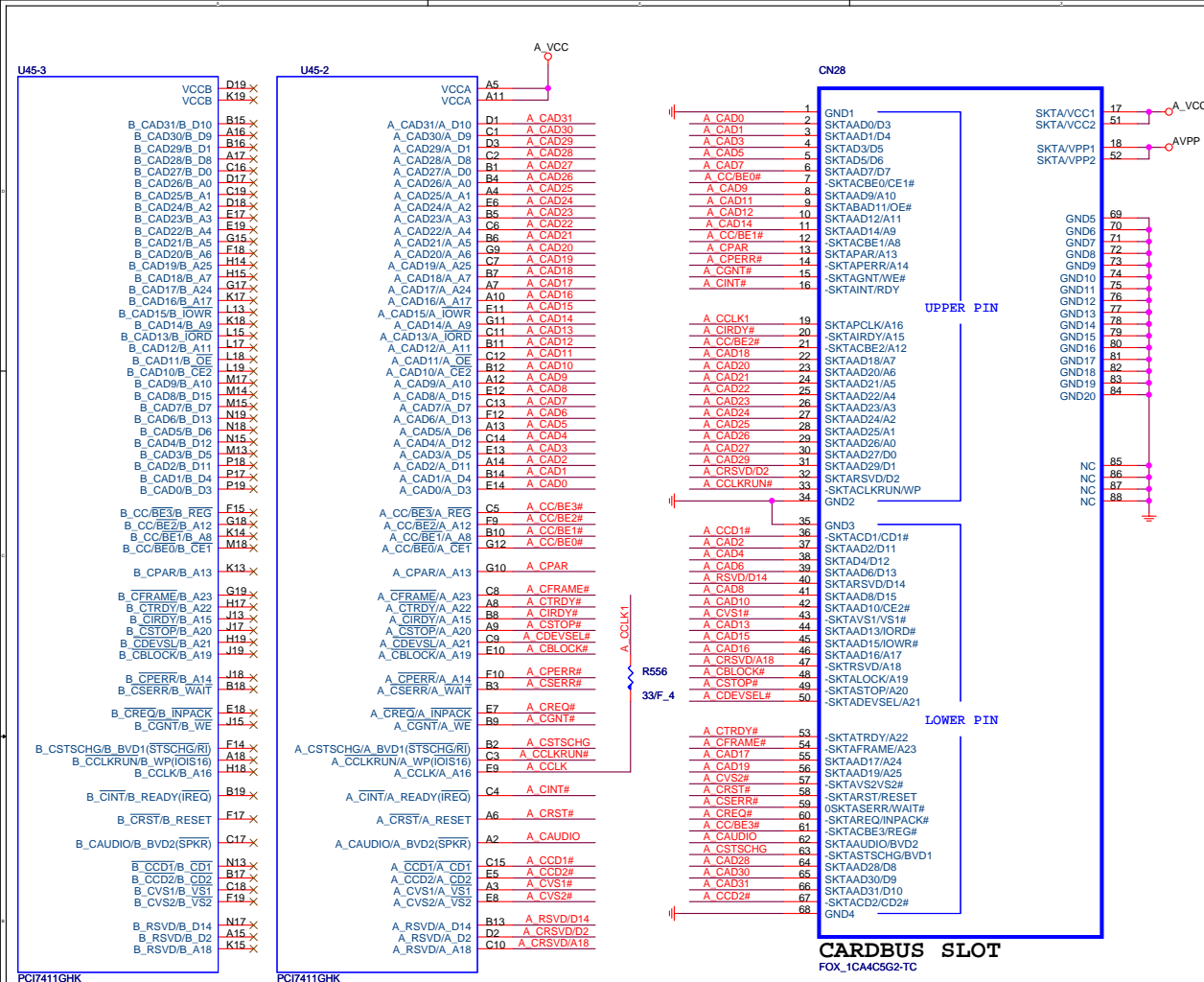


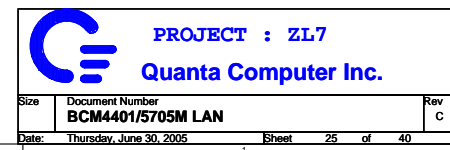
 <div style="text-align: center;"> <b>PROJECT : ZL7</b>  <b>Quanta Computer Inc.</b> </div>		
Size	Document Number <b>HDD &amp; CDROM &amp; MEDIA BAY</b>	Rev <b>C</b>
Date:	Thursday, June 23, 2005	Sheet 21 of 40

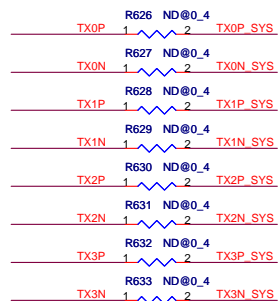
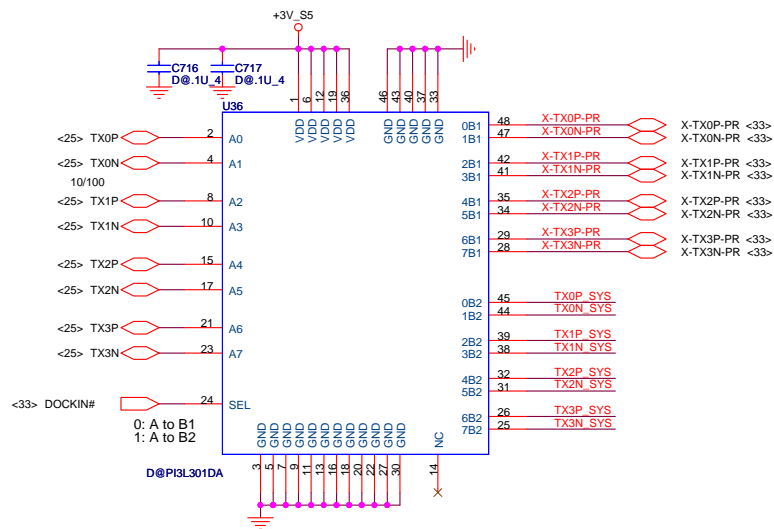




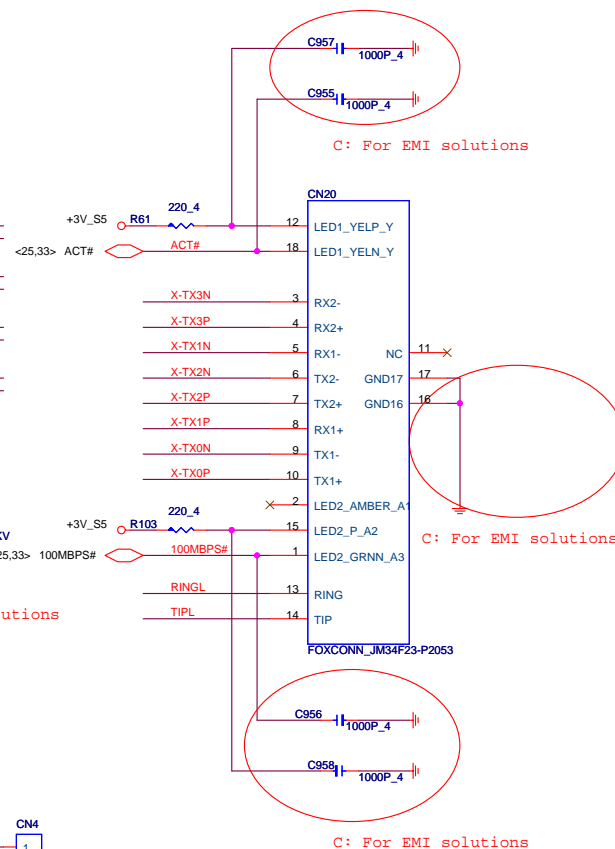
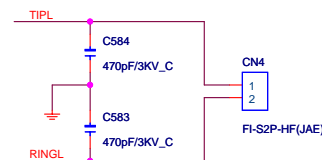
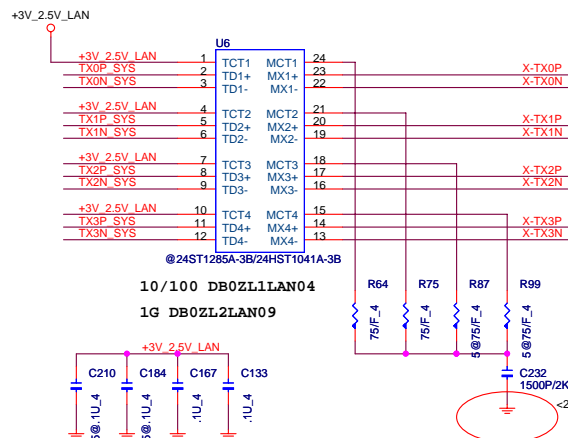






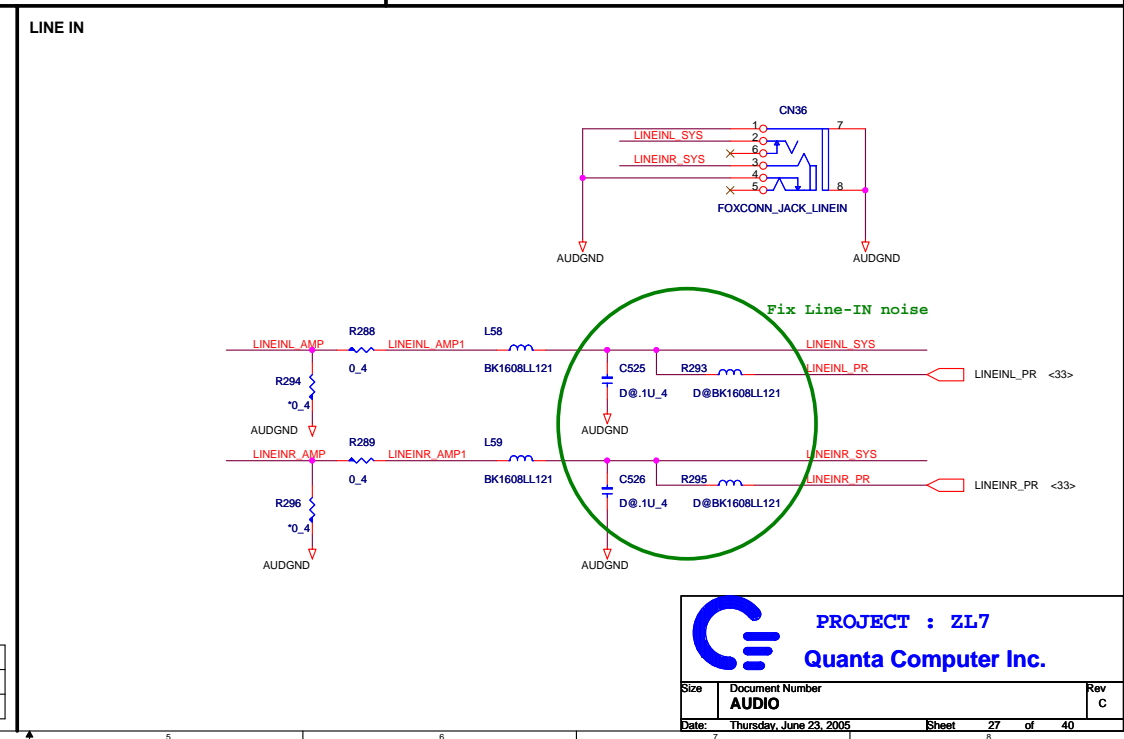
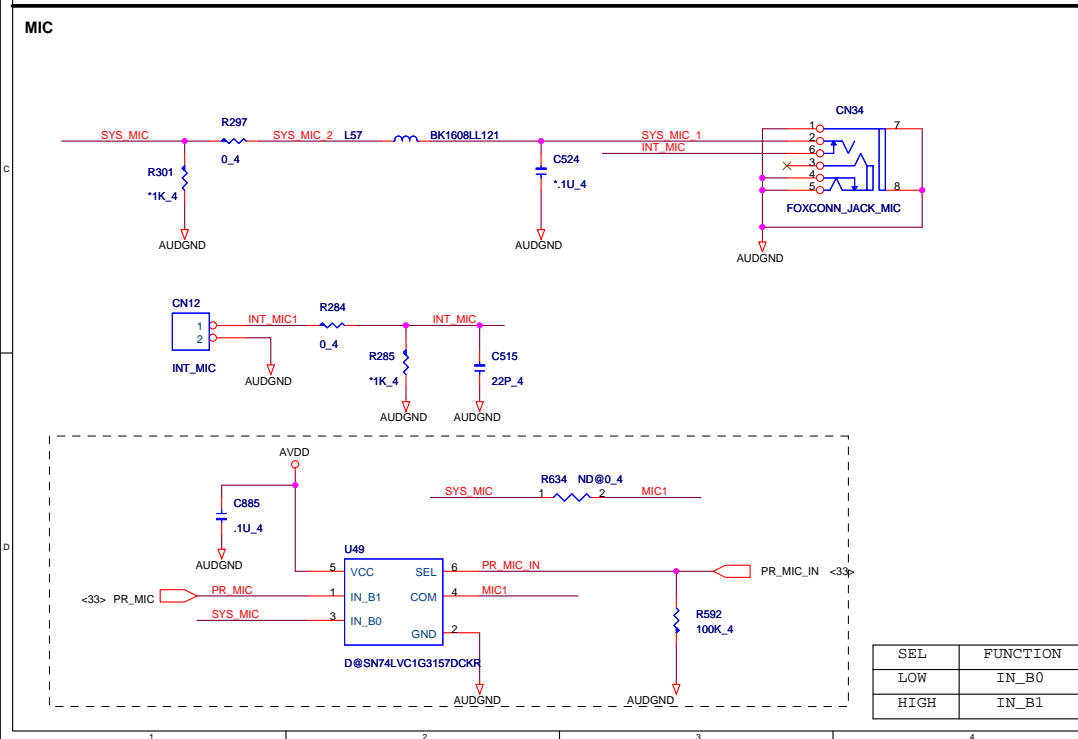
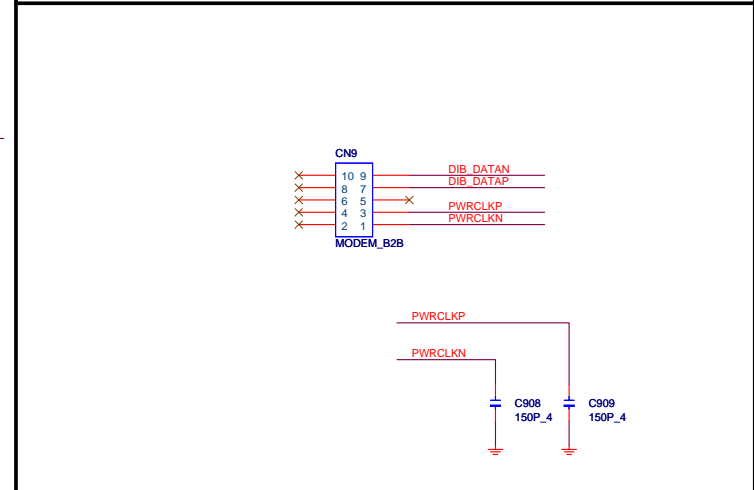
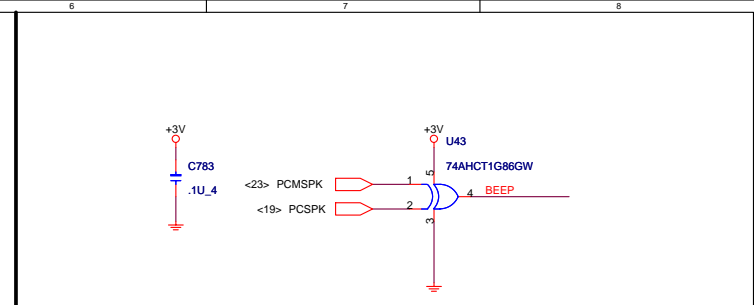
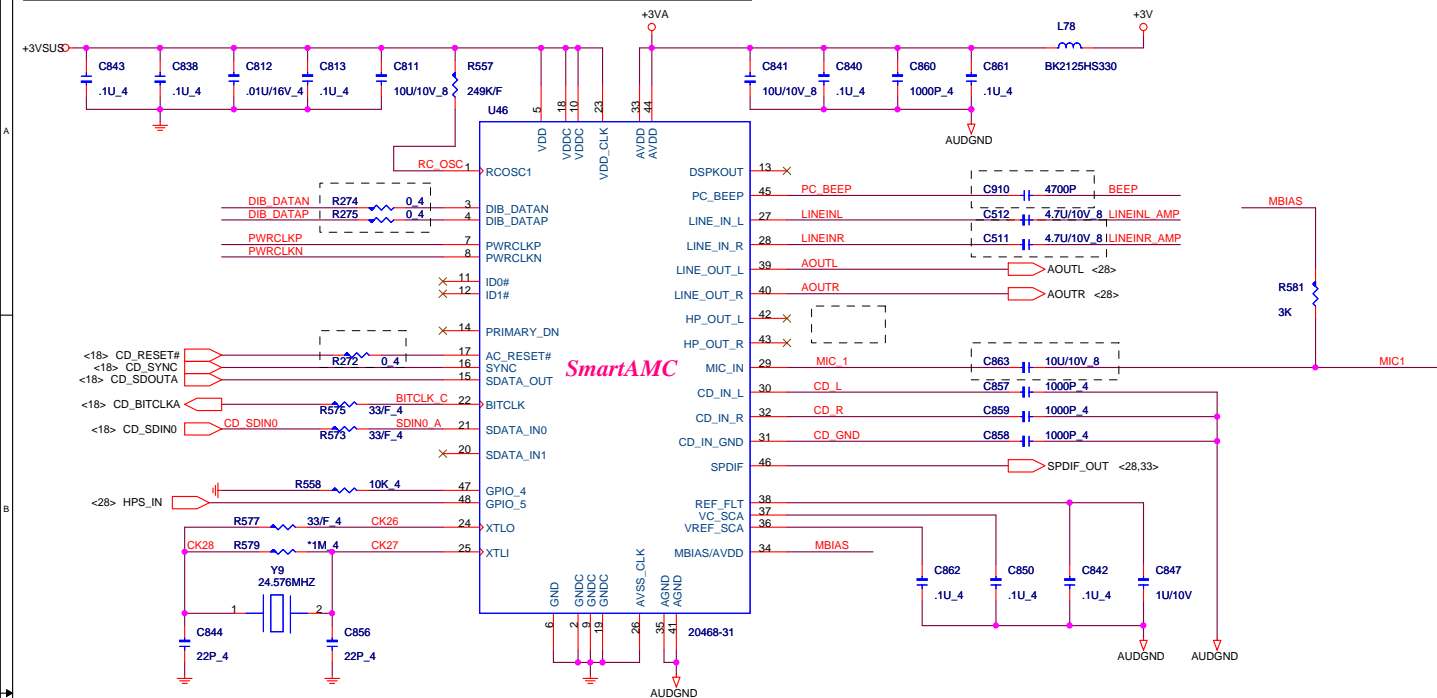


ADD CIRCUITS WHEN NO DOCKING



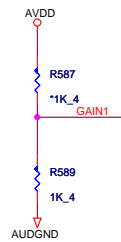
PROJECT : ZL7  
Quanta Computer Inc.

The AMC20463-004 modem is used for mother board family MBAMC20463-004.

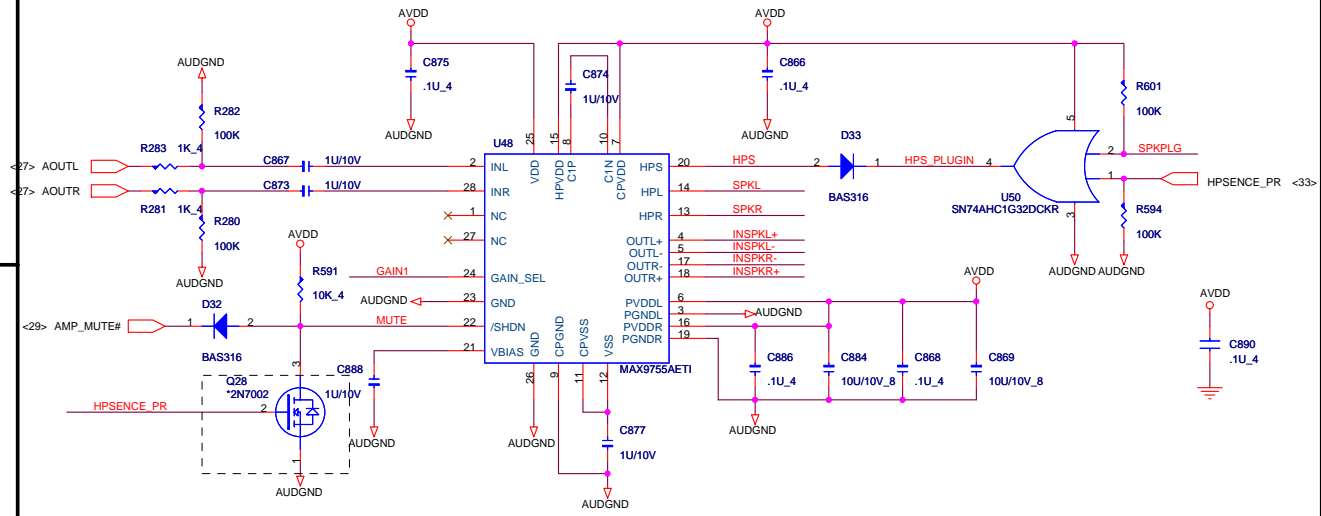
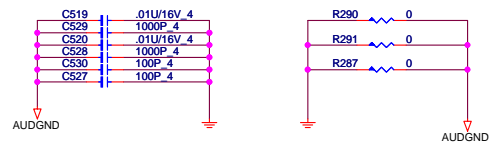
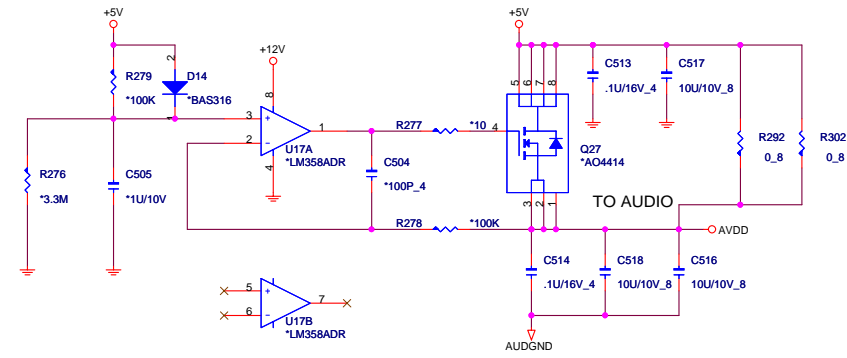


SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1

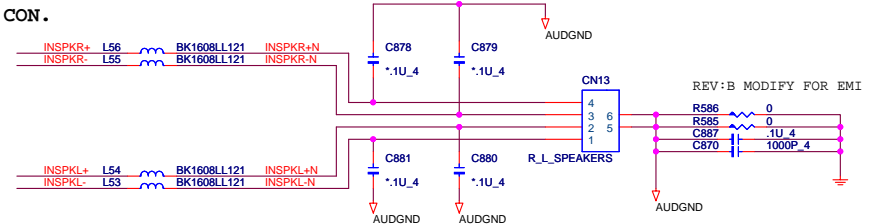
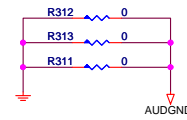
GAIN1	SPKR MODE	HP MODE
0	10.5	3
1	9	0



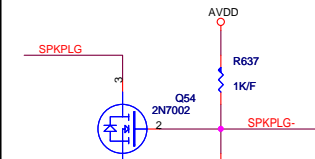
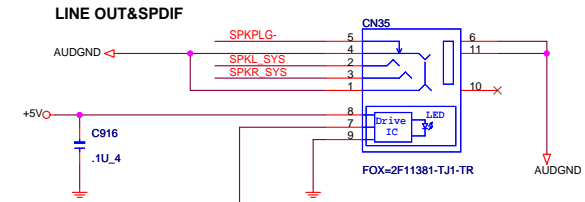
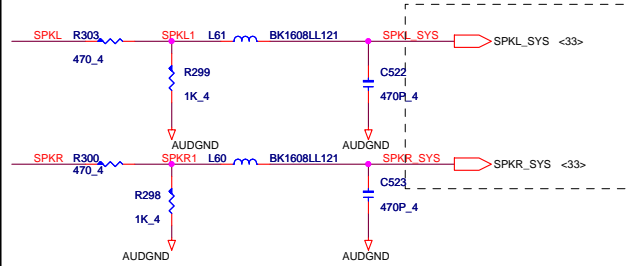
# AMP POWER



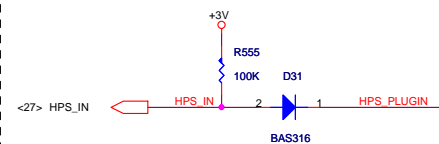
# SPEAKER CON.



# LINE OUT&SPDIF



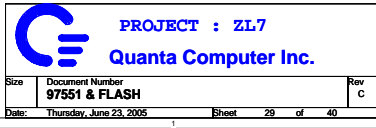
NEW ADD FOR ESD  
CLOSE TO CN35

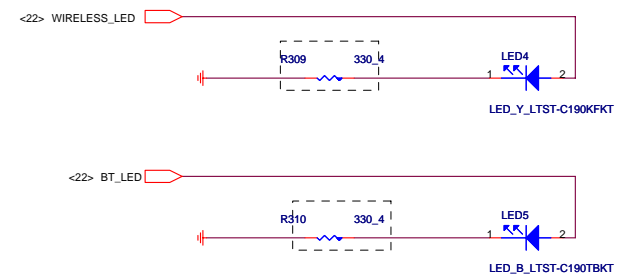
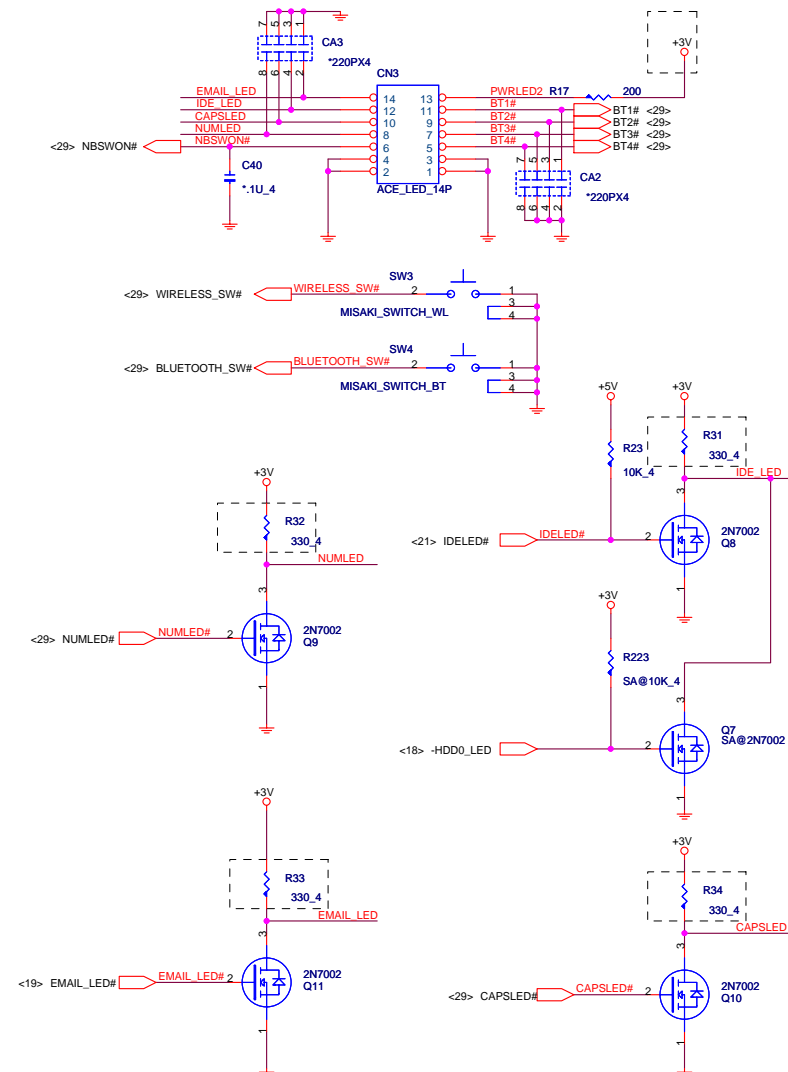
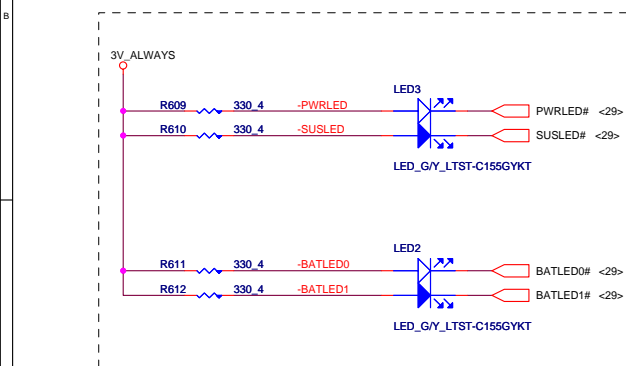
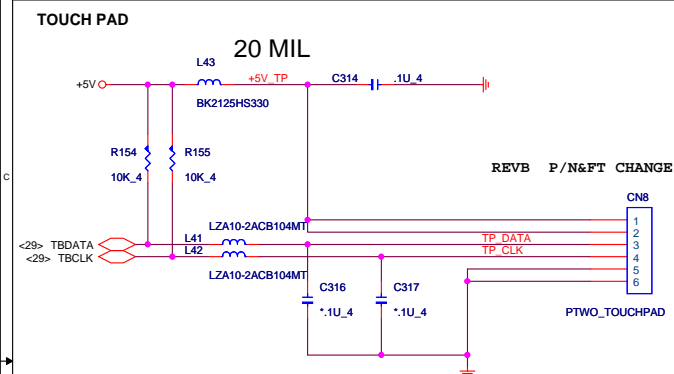
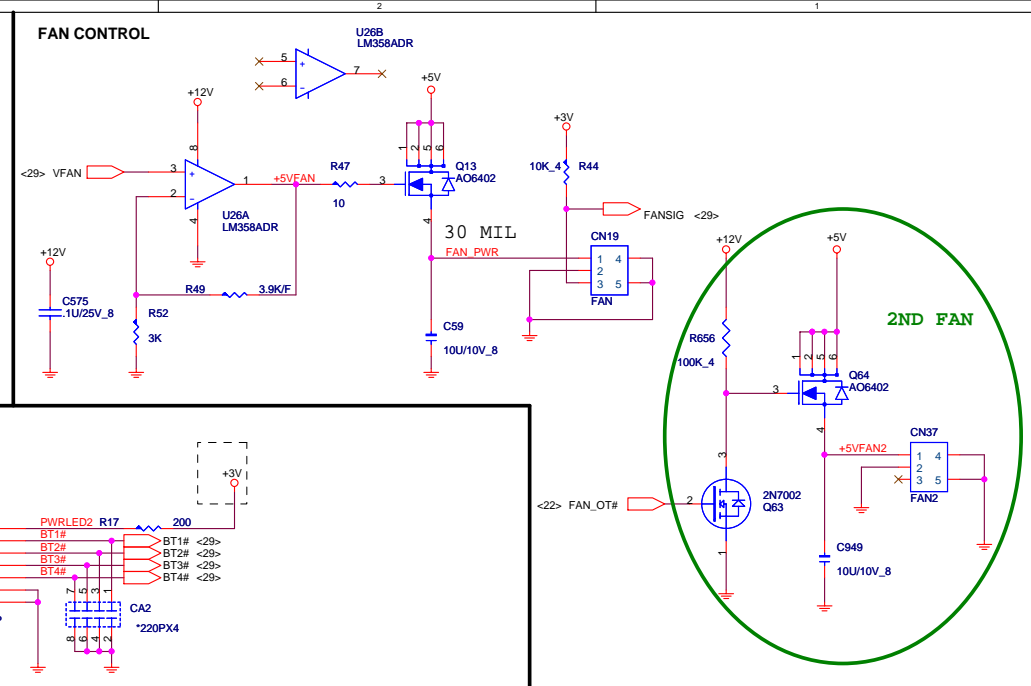


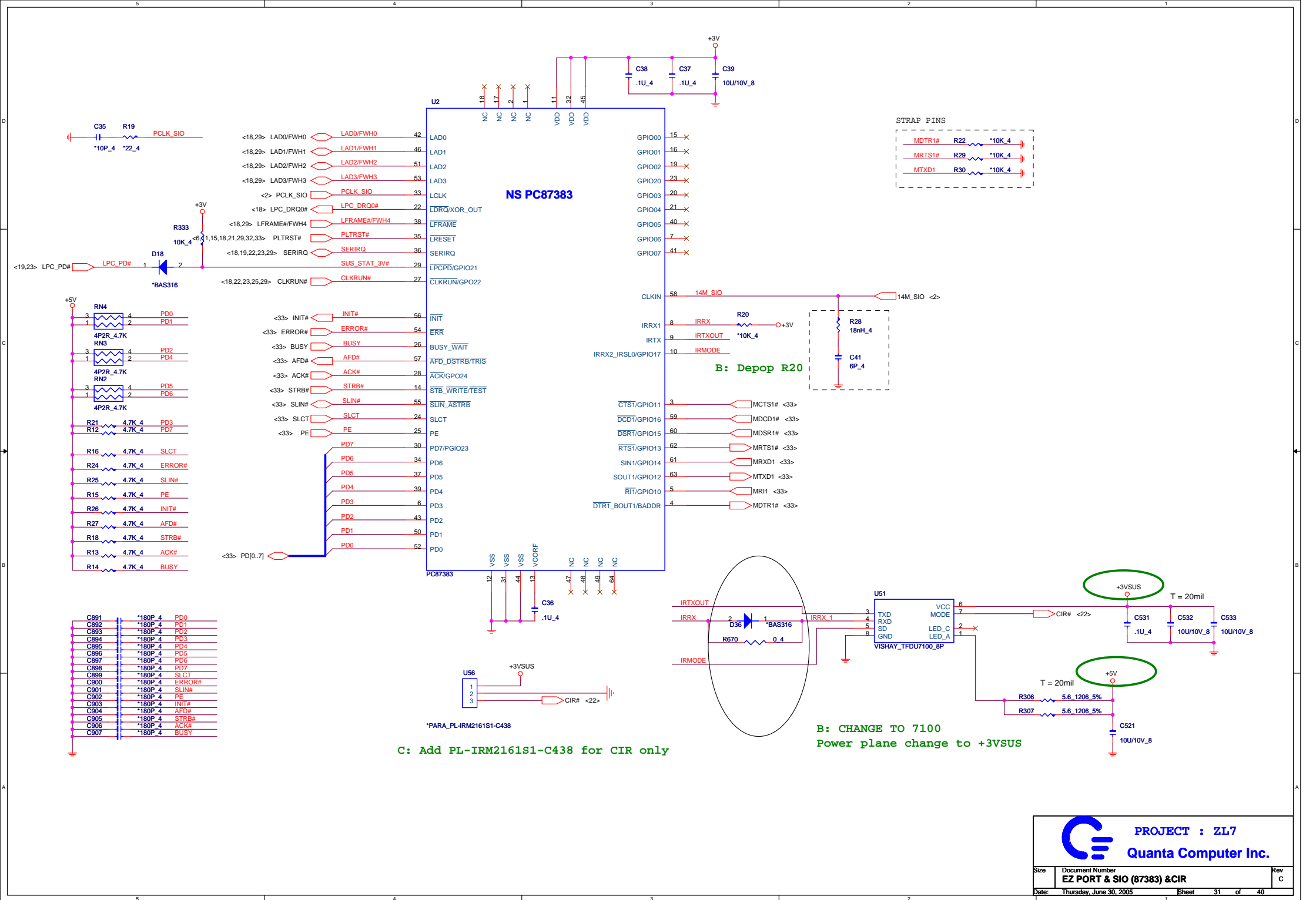
PROJECT : ZL7  
Quanta Computer Inc.

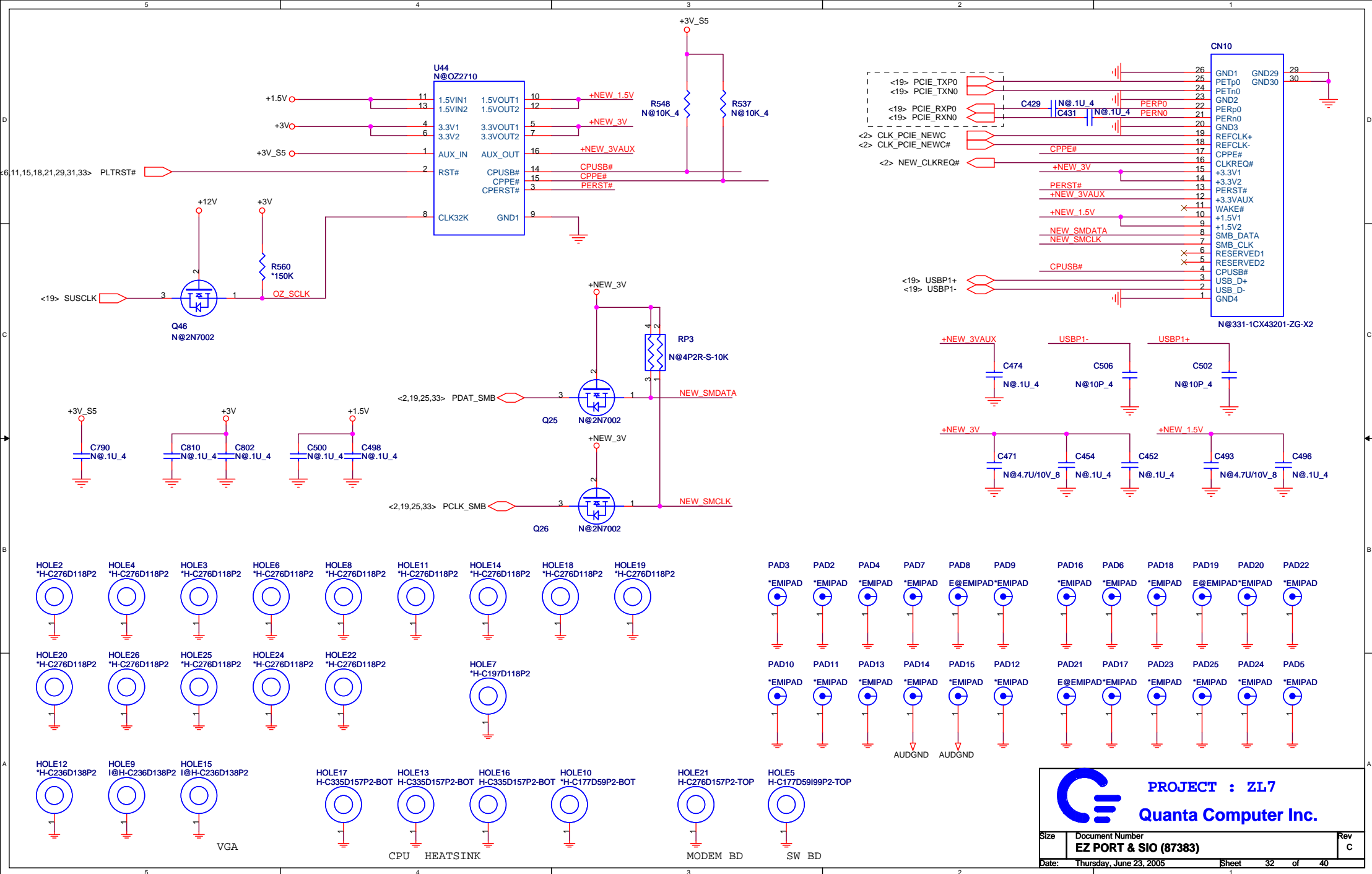
Size	Document Number	Rev
	AUDIO AMP	C
Date:	Thursday, June 23, 2005	Sheet 28 of 40

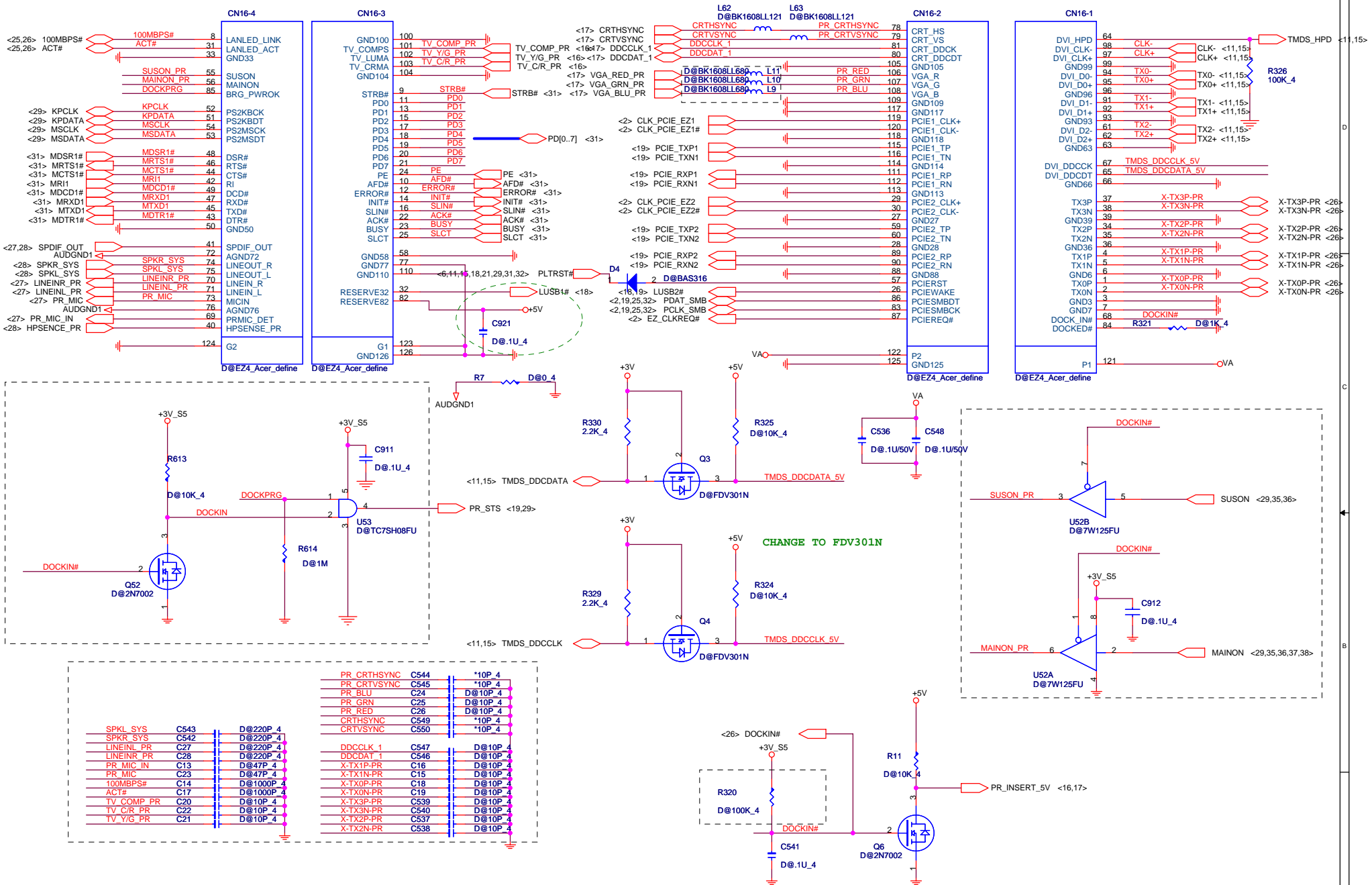




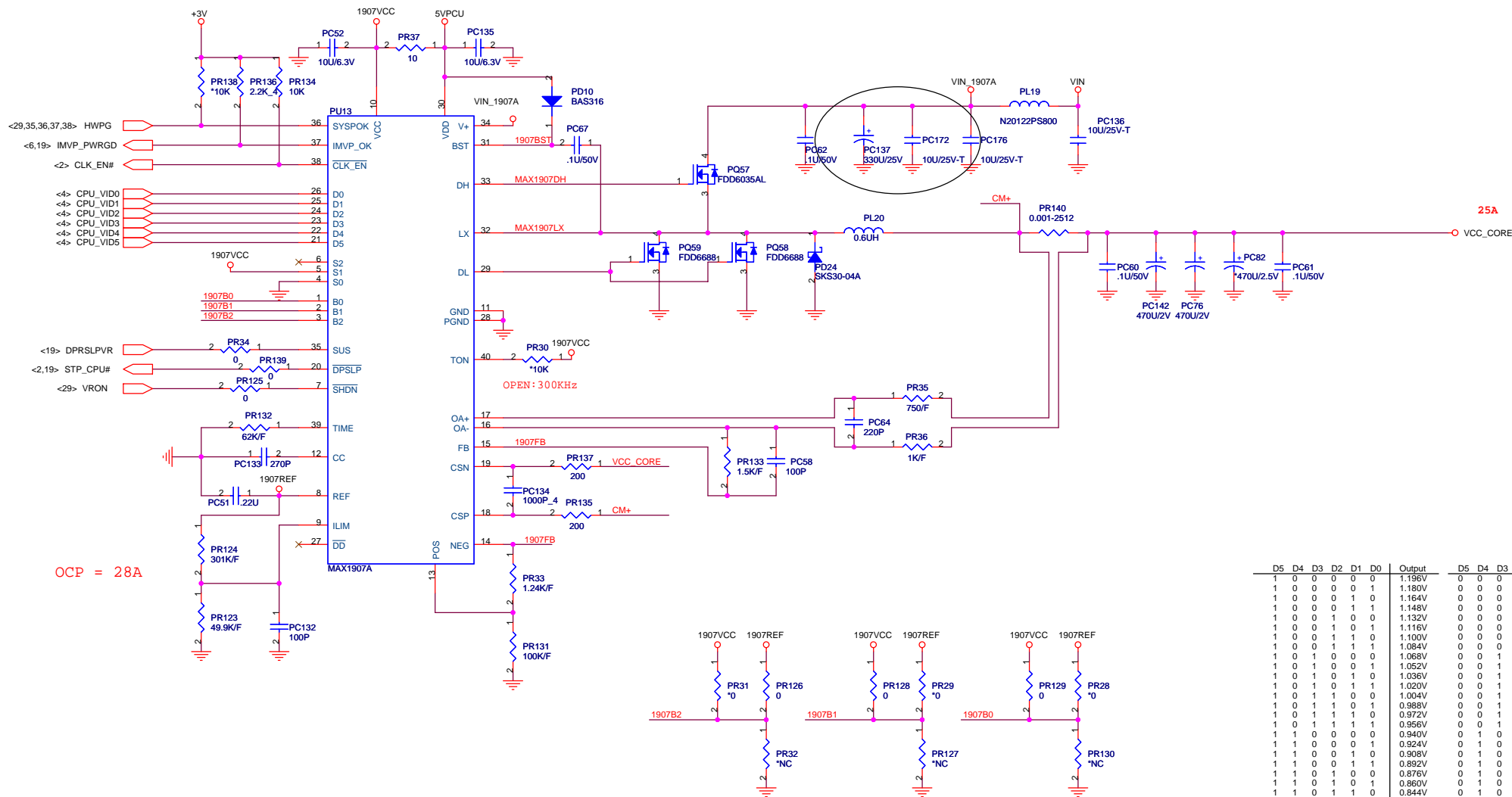








PROJECT : ZL7  
Quanta Computer Inc.



OCP = 28A

OPEN: 300KHz

SUSPEND MODE (SUS=HIGH)

S2	S1	S0	Output
✓ OPEN	VCC	GND	0.748V

VCC\_BOOT

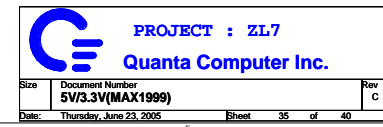
B2	B1	B0	Output
GND	GND	GND	1.708V
REF	REF	REF	1.372V
OPEN	OPEN	OPEN	1.036V
VCC	VCC	VCC	0.700V
REF	VCC	VCC	1.212V

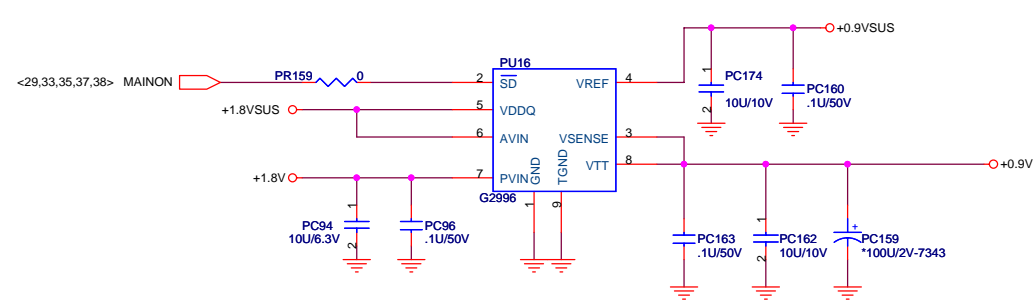
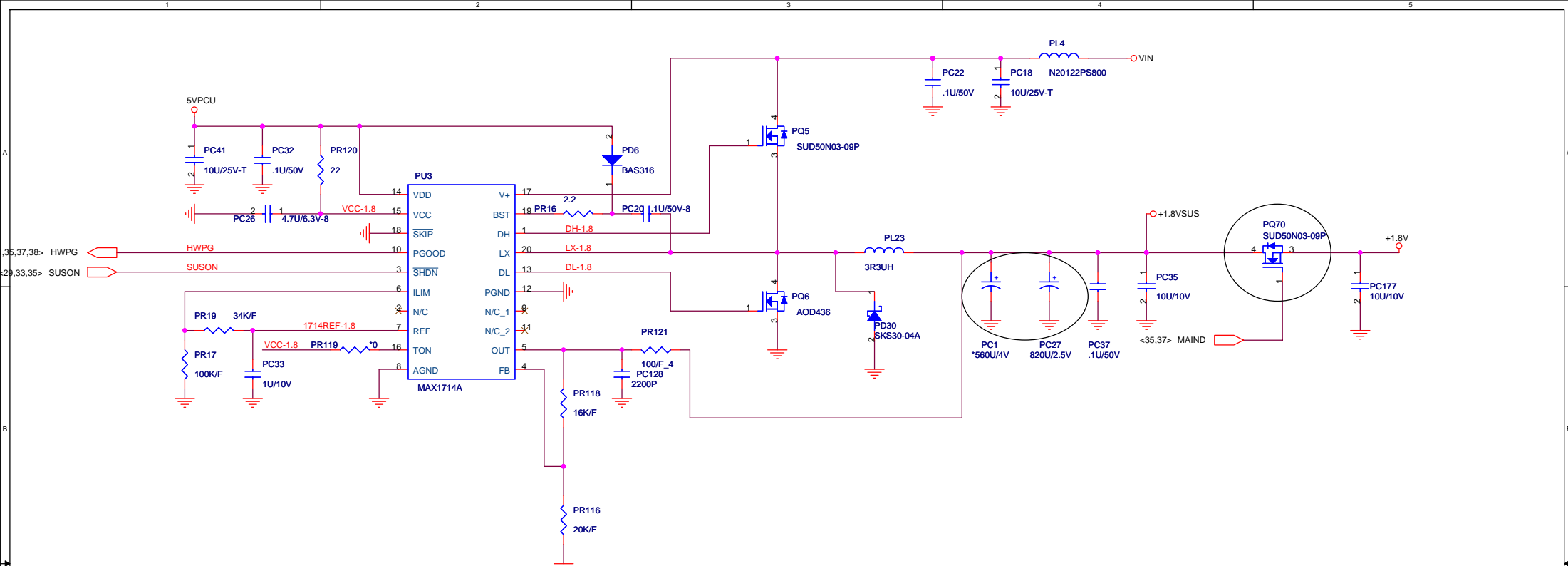
D5	D4	D3	D2	D1	D0	Output	D5	D4	D3	D2	D1	D0	Output
1	0	0	0	0	0	1.196V	0	0	0	0	0	0	1.708V
1	0	0	0	0	1	1.180V	0	0	0	0	0	1	1.692V
1	0	0	0	1	0	1.164V	0	0	0	0	1	0	1.676V
1	0	0	0	1	1	1.148V	0	0	0	0	1	1	1.660V
1	0	0	1	0	0	1.132V	0	0	0	1	0	0	1.644V
1	0	0	1	0	1	1.116V	0	0	0	1	0	1	1.628V
1	0	0	1	1	0	1.100V	0	0	0	1	1	0	1.612V
1	0	0	1	1	1	1.084V	0	0	0	1	1	1	1.596V
1	0	1	0	0	0	1.068V	0	0	1	0	0	0	1.580V
1	0	1	0	0	1	1.052V	0	0	1	0	0	1	1.564V
1	0	1	0	1	0	1.036V	0	0	1	0	1	0	1.548V
1	0	1	0	1	1	1.020V	0	0	1	0	1	1	1.532V
1	0	1	1	0	0	1.004V	0	0	1	1	0	0	1.516V
1	0	1	1	0	1	0.988V	0	0	1	1	0	1	1.500V
1	0	1	1	1	0	0.972V	0	0	1	1	1	0	1.484V
1	0	1	1	1	1	0.956V	0	0	1	1	1	1	1.468V
1	1	0	0	0	0	0.940V	0	1	0	0	0	0	1.452V
1	1	0	0	0	1	0.924V	0	1	0	0	1	0	1.436V
1	1	0	0	1	0	0.908V	0	1	0	0	1	1	1.420V
1	1	0	0	1	1	0.892V	0	1	0	0	1	1	1.404V
1	1	0	1	0	0	0.876V	0	1	0	1	0	0	1.388V
1	1	0	1	0	1	0.860V	0	1	0	1	0	1	1.372V
1	1	0	1	1	0	0.844V	0	1	0	1	1	0	1.356V
1	1	0	1	1	1	0.828V	0	1	0	1	1	1	1.340V
1	1	1	0	0	0	0.812V	0	1	1	0	0	0	1.324V
1	1	1	0	0	1	0.796V	0	1	1	0	0	1	1.308V
1	1	1	0	1	0	0.780V	0	1	1	0	1	0	1.292V
1	1	1	0	1	1	0.764V	0	1	1	0	1	1	1.276V
1	1	1	1	0	0	0.748V	0	1	1	1	0	0	1.260V
1	1	1	1	0	1	0.732V	0	1	1	1	0	1	1.244V
1	1	1	1	1	0	0.716V	0	1	1	1	1	0	1.228V
1	1	1	1	1	1	0.700V	0	1	1	1	1	1	1.212V

**PROJECT : ZL7**  
**Quanta Computer Inc.**

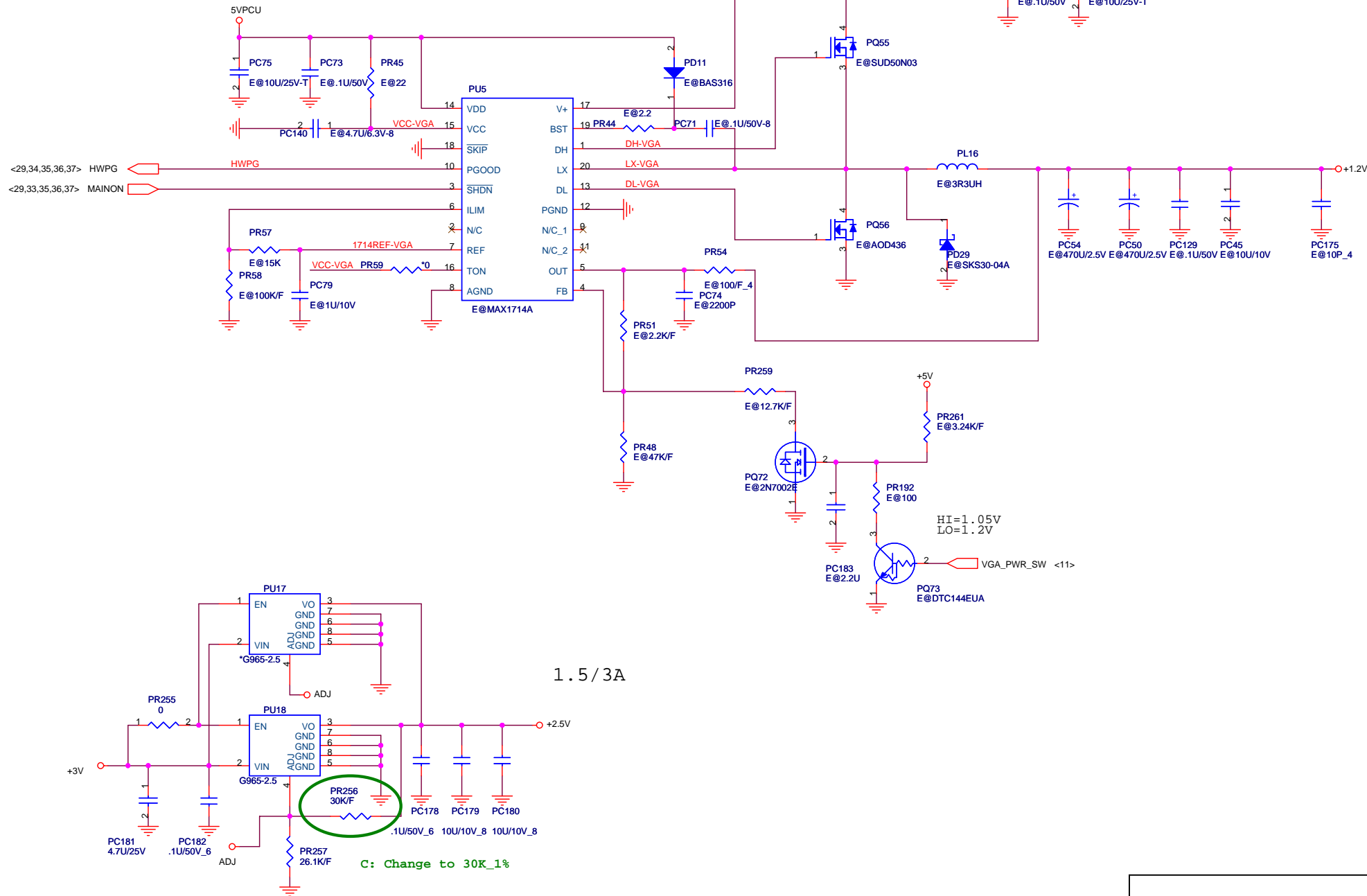
Size	Document Number	Rev
	<b>CPU CORE (MAX1907)</b>	C
Date:	Thursday, June 23, 2005	Sheet 34 of 40



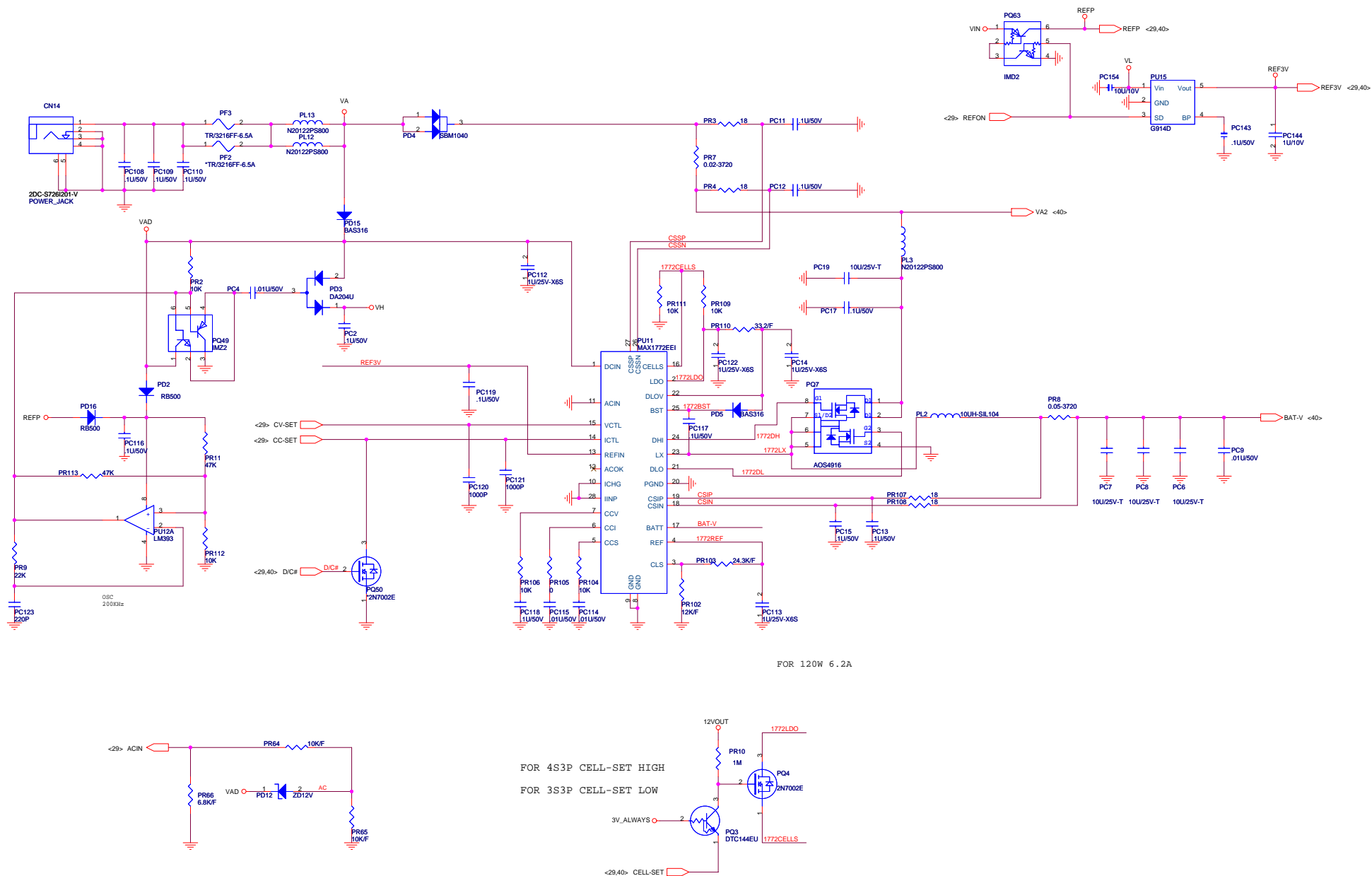




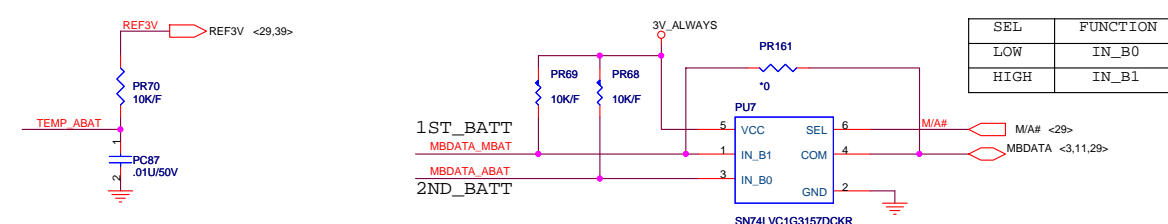
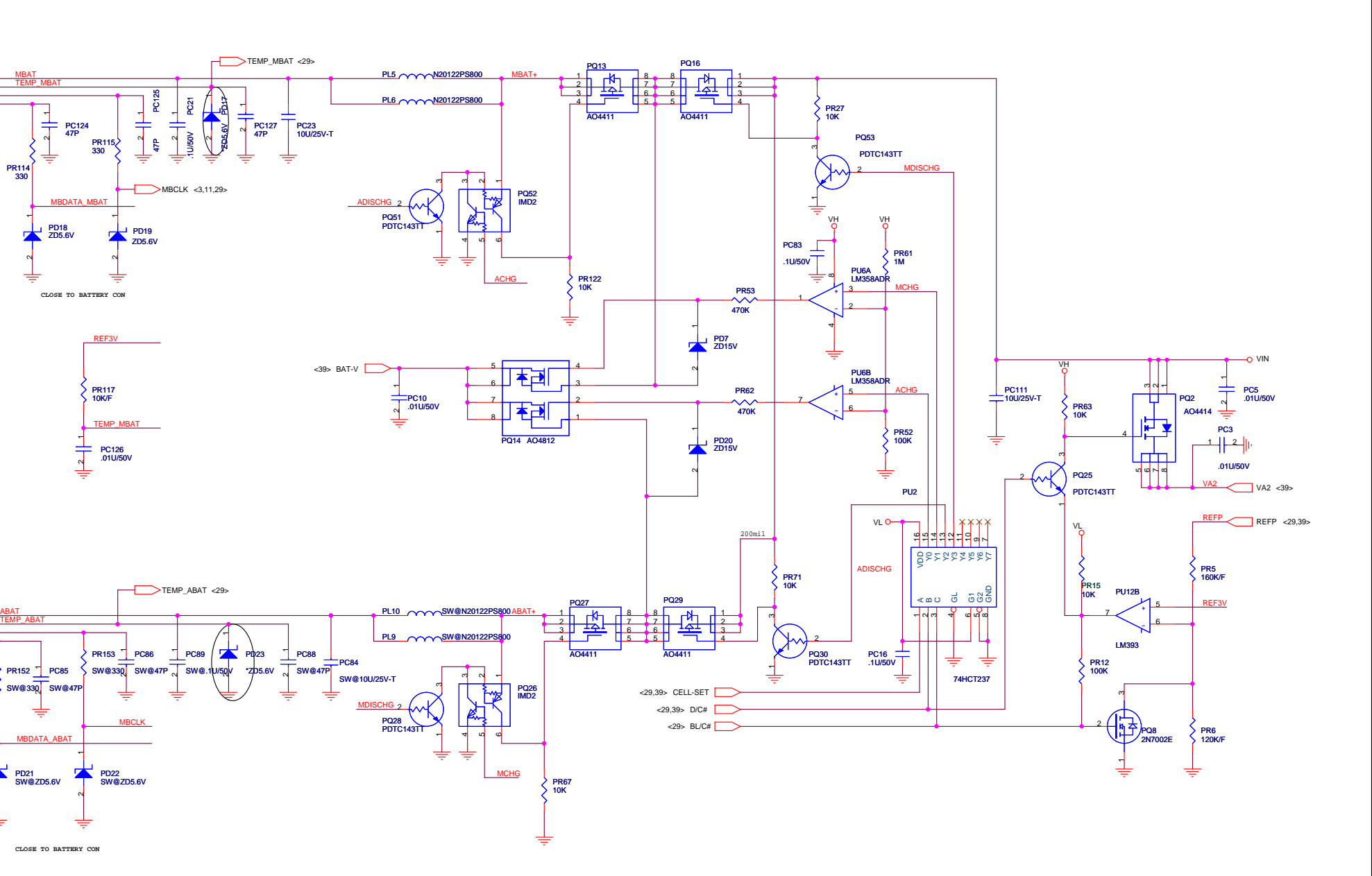




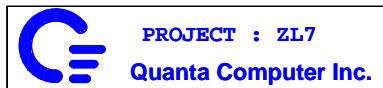
Size	Document Number	Rev
	<b>+1.2V/+1.8V</b>	C
Date:	Thursday, June 23, 2005	Sheet 38 of 40



1	2	3	4	5
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SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1



Size	Document Number <b>BATTERY SELECT</b>	Rev C
Date:	Thursday, June 23, 2005	Sheet 40 of 40